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Intentional Software Product Line

Techniques against Various Attacks

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Laboratory, Antarctica

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Architecture and Hardware Solutions Symbolic Information Processing

By Ibrahim Al-Turani, Dr. Eugene Titenko, Dr.Nabeel Zanoon

Applied University, Jordan

Abstract - The failure of some national projects AXES to expected results. According to experts one of the reasons is the lack of adequate theoretical apparatus for generating high-branching processes, unjustified detraction of opportunities enumerative models representative of AXES, which have their own laws paralleling computing, can not be reduced to algorithmic rules. This fact determines the need to develop innovative approaches to problem solving and organization of AXES interrelated levels of system design symbolic computation, from the linguistic level to the appropriate software and appropriate hardware level.

Keywords : symbolic information, artificial intelligence, Flow control, Architecture.

GJCST Classification: B.1,H.3.2



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Architecture and Hardware Solutions Symbolic Information Processing

Ibrahim Al-Turani^{α}, Dr. Eugene Titenko^{Ω}, Dr.Nabeel Zanoon^{β}

Abstract - The failure of some national projects AXES to expected results. According to experts one of the reasons is the lack of adequate theoretical apparatus for generating highbranching processes, unjustified detraction of opportunities enumerative models representative of AXES, which have their own laws paralleling computing, can not be reduced to algorithmic rules. This fact determines the need to develop innovative approaches to problem solving and organization of AXES interrelated levels of system design symbolic computation, from the linguistic level to the appropriate software and appropriate hardware level.

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I. INTRODUCTION

ccording to estimates of scientific authority in the modern computer technology, one of the leading trends of development of computer technology is the creation of homogeneous processing devices and information storage for the supercomputer and multiprocessor systems (MPS), focused on the implementation of parallel computing [1]. It is known that the main object processing symbolic information stands in various models of representation [2]. In this regard, the strategic relevance for the design supercomputers IBM and (MPS) are the questions of creating methods, architectures, and circuit design alternative for intelligent processing of information. The importance of models and techniques for intelligent of information associated with the processing emergence and rapid development of artificial intelligence (AI), in which we study a model of knowledge representation and processing. The main substantive aspects of AI problems and processes of AXES in them are the following. First, the basic format of knowledge representation and processing is currently a character format, essentially having a higher level of organization of parallel computations than the number format. Second, efficient processing of knowledge - is the realization of constructive processes of branching for multiple data with a variety of parameters, the dynamic variation of the structure and size of the data instances, by default require non-standard multiprocessor architecture tour.

Originating in the 80th years of XX century as an independent branch of computer science, computer systems have traditionally included in the AXES interdepartmental, national and international scientific and technical programs and projects to create computer systems and new generation, that determines the strategic importance of research and processing of symbolic computation knowledge. Notable historical examples of such programs and projects are the West-European project ESPRIT. an American project ALVEY. Japanese project to create the fifth-generation machines (1982-1991) And the Japanese project to create a computer with a "fluid intelligence", the Russian project to create a supercomputer "Elbrus", etc. strategic importance to the problems of AXES gives a constant interest defense ministers of key countries, linking national security with the advanced development of parallel computing systems based on non-traditional (non-von Neumann) architecture.

Some national projects AXES did not lead to the expected results of my. According to experts one of the reasons is the lack of an adequate theoretical apparatus for generating high-branching processes, unjustified disparagement opportunities enumerative models representative of AXES, which have their own laws parallel computing, can not be reduced to algorithmic rules. This circumstance determines the need to develop fundamentally new approaches to solving problems of AXES and the organization of interrelated levels of engineering systems, symbolic computation, from the linguistic level to the appropriate software and hardware level.

II. METHODS OF MANAGING THE PROCESSING OF SYMBOLIC INFORMATION

The tasks of AXES with the elements of intelligent computing are understood as a search problem and the parallel generation of new states from the available set of initial states and a set of mathematical rules that are permissive nature of the execution, i.e. based on enumerative systems. On one side, the term "permissive rules" enumerative system is specified in accordance with the agreement of Post as an alternative to firing rules. For this reason, many treatment options, and constructive processes enumerative branching system simulates the algorithmic

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system with a serial product of branching processes in a linear space-time. Other hand, enumerative systems, the term "permissive rules" can be specified as equal to the firing rules [4, 5]. The consequence of this method of refinement is a parallel implementation of branching processes with a structural mechanism for the generation of new states permanently to the desired number of copies of copies of data.

Nevertheless, the implementation of parallel computations on an equal basis associated with the dynamic generation of specific objects that provide a quantitative assessment of the branching process along different trajectories computation. Static methods parallelism inherent in processing numerical information, based on the placement of a dynamically modifiable set of branching processes in homogeneous computing modules. For problems of AXES such methods are not applicable in due to the lack of reliable information about the structure of a graph problem AXES. Obscurity graph structure calculations also leads to the substitution of enumerative system on its equivalent algorithmic model and unproductive expenditures of time series-return mechanism of generation of new states in the search graph [6]. These differences between numeric and symbolic computations on a theoretical level, make it necessary to use different system architecture solutions, and micro-level implementation of the A subsystem of parallel symbolic computation.

A subsystem at the level of implementation of parallel computing are three control method of the computing process, consisting of many interacting flows: flow control commands (the traditional von Neumann method), flow control, flow control requirements (switching).

Flow control commands have limited opportunities to engage in branching processes, AXES, as additional time required for the dynamic placement costs between sub-cores of the system and loss of time for data synchronization. At the same time with increasing number of processor cores total load factor of the system is significantly reduced and the problems of real complexity is reduced to 5-10% [3]. These values do not allow us to consider how the flow control commands as a promising option for the computer systems AXES

For parallel computing is a potentially attractive model calculations, flow control [3]. According to this model, any computational process is directed graph of data flow. In this graph nodes (vertices) are computer operators, and the arcs of the graph moving special data structures – Tokens. Special structures (tokens) contain field offices, describing the formats and types of operands. The coincidence of the operands to the format and automatically determines the type of command being executed, and its readiness to perform. Refusal of addressing memory cells and the transition to

management through conformity the various data fields token characterized the fundamental difference between computers of AXES from the machines with the von Neumann architecture for A subsystem level. The detection of all operands relating to the common vertex of the graph is executed by named tops is an indication of initialization and computation processing unit for the given tops. Such principle of the manage eliminates the problem of synchronization and racing flows, provides an asynchronous data flow promotion by pipelined to the ring of computers that control the flow of Composition operators computing, data. communications between nodes in a graph are defined in advance at the stage of writing the program, thereby setting the graph structure. Computers Architecture Data Flow Implement the direct execution of the graph. It is provides parallelism of computing processes, given program, and excludes the conflict situations in these. The main feature of the model calculations, flow control. command execution is not on the counter, and when ready input operands for the current nodes of the graph. Execution of this Rule leads to an asynchronous execution of multiple commands at the nodes of the graph, which resulted in its input from the arcs are absorbed, and the output arcs are generated by the results of computations in a node.

This way, parallel computing model on the data flow using a limiting parallelism peculiar task that meets the requirements of the tasks of the AXES in the generation of high-branching processes with varying duration of execution. Another feature of the competitive method and models of flow control is to use a of homogeneous set of devices on an equal basis, that provides the maximization of load devices in the asynchronous command execution on parallel graph computation, At the same time known the data flow machines (MIT SDA, MDFM, MIT TTDA (England), LAU System (France), NEC Image Pipelined Processor (Japan), and others) are still oriented to the processing of numerical information, which is characterized by an explicit task graph computations In contrast, the AXES the task in most cases are not finished making a graph that defines the limitations of direct accepts of the method of flow control.

Flow control requirements is a hybrid variant control that is based on the union of a sequence of commands in a single unit with a control in its flow control and flow control between blocks of commands. In essence, task is described poorly connected graph macro level (block commands) having a low rate of exchange flows between macro level.

	Flow control commands	Data flow control	Flow control requirements
description	The usual execution of the operators at their place of in the control system	"Greedy" execution of all operators for which all operands are available	"Lazy" execution of the operators, without which there can be the results of the further calculations
advantages and	Full control Easy to realization of complex data structures and control structures	The high degree of parallelism high performance	Execution only the necessary operators The independence of computing
disadvantages	low efficiency complexity programming	Complexity of control data structures Costs of unnecessary storage resources on the operands complexity of control	Time costs for the transfer of markers The complexity of public access to the structures of local representation

Table 1: Ways to control sub system level

III. FUNCTIONAL NODES AND CIRCUIT SOLUTIONS FOR IBM AXIS

The known of hardware solutions can be classified as AXES in their relation to micro-level hardware solutions (circuit design implementation), A subsystem (structural and functional organization), and systemic levels (a common system architecture AXES).

At the micro level we are talking about the functional nodes, blocks, and device-properties of that support basic operations, elements of the programming language in their simplest form. On the one hand, these functional units have a rigid specialization and poorly suited for general-purpose microprocessors with software control. On the other hand, commonality of processes manipulation of symbols as with abstract images, belonging to the basic pattern of thinking, "condition-action" allow us to consider these functional units as the basis for computer AXES . Availability and use of abstract computing systems (machines) for manipulating the symbols will lead to the formation of self-class operating devices with non-traditional organization extend the instruction set of modern microprocessors. Digital Converters character-oriented branching symbolic computation and the generation of a set of symbolic structures (form image shape), ultimately, justify the existence of the justification of individual devices, high-performance machines and systems that process symbolic information and knowledge, as opposed to parallel processing of numeric data and numeric computer IBM.

As a promising technical solutions for computer systems AXES level devices and functional units [1, 3] the leading scientists in the field of view Tues hardware blocks (**Table 2**) for standard operations of AXES. Continue to comment on possible hardware units for the axis and branching of computational processes.

Table2 : AXES operations circuitry-level realization

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AXES operations	hardware blocks
Calling functions recursion	hardware stacks
	register window
	Operating register-memory
typing of data	tagging of memory
	The apparatus of parallel
	testing tags
sorting	VLSI-graders
Pattern matching,	finite state machines
identification	associative memory device
	matrix Converters
Modification of the	associative memory device
fragments of character	character tasovateli
structure, reconfiguration	The positional shift memory
garbage Collection	Multifunctional VLSI-
	graders
handling multiple response	Distributed hierarchical
	arbiters
Binary substitution	Iterative schemes for
character data	processing unitary codes

Hardware stacks, and methods for quick access to the stacks are designed to speed up function calls. This is especially useful for functional paradigm of AXES and the family of programming languages LISP. Quick operations with the stack are also useful in the implementation of Prolog. When backtracking made numerous write operations on the stack and reading from the stack.

The current stage of development of the functional units of AXES involves the use of new abstract data structures (Table 1), such as deck. To control the pointer on the group of available vertices:

Another circuitry organization associated with the creation of hybrid structures to store and access items on a stack-based organization, complemented by:

1. The reconfiguration of the stack to the associative

structure with a parallel search and access.

 Reconfiguration of the stack in a hierarchical structure of the shear to jump to the "deep" elements of the stack.

The following non-standard functional unit intended for computer IBM AXES is tagged memory. In conventional computer IBM Von Neumann type does not distinguish between data and program, which are stored as binary strings of fixed length. Semantics of the data determines only manipulated by the program rather than the actual contents of memory, which are stored as binary strings of fixed length. Semantics of the data determines only manipulated by the program rather than the actual contents of memory. In contrast, involves a self-sufficient representation tagged representation at all levels of memory. Currently, tagging- a powerful mechanism and hardware-software tool data typing, management calculations. The most significant use of tagged memory associated with the model calculations, flow control, and machines and data streams (first of all Manchester Data Flow Machine [3]).

The most common method of hardware realization of data tagging is to add a few bits of each word, determine its type. Check the type of data in the process can be supported by additional hardware, first of all associative memory, and perform the selection of priorities and amputation unpromising branches in the graph algorithms. A distinctive feature of tagging is the task type the command being executed. Special structures (tokens) contain field offices, describing the sizes and types of operands. The coincidence of the operands by size and type of data fields in memory command automatically determines the type of command being executed. Refusal of addressing memory cells and the transition to management through compliance data fields characterizes the fundamental difference between computers AXES from the machines with the Von Neumann architecture.

The third major operation AXES - hardware support for pattern matching. Analysis of empirical data shows, Up to 90% of the time of the enumerative production system (PS) in expert systems can be spent on the process of mapping, iterative nature of the bearing. In such a way hardware realization of this operation leads to more efficient generation of branching processes and the character generating a set of candidates solutions to common principles.

Hardware processing blocks unitary binary codes. Finally, a distinguishing feature of the processes and objectives AXES is to replace character-format data unweighted binary code, in particular in the problems of searching, comparing, and comparison, identification of character data and other operations of the higher forms of computing. Such codes were named as the unweighted unitary codes, they are characterized by the possibility of applying logic and arithmetic operations, and specific processing is not dependent on the size of the bit lengths of codes. Typical examples are the functional units of digital compressors, comparators, code converters. These sites, along with high-speed distributed by the arbitrators, are the basis of responses of multiple processing units.

Character shuffle for problems-axis is not sufficient to investigate the organization of branching in symbolic computation is the use of switching converters Data, oriented on structural change in the relations between the elements. Switching converters information regarding the functional units of numerical processing is not widely used, while symbolic calculations are based structural transformation of local or global. They are associated with dynamic changes in the computation of relations of subordination or repetition of elements of symbolic structures. Structural reforms could be considered as the composition of the reconfiguration of the data structures and inter-element rearrangements controlled in these structures.

Hardware support for this operation seems justified to use the matrix and Hypercube organizations operating parts to create two-and multi-operand switching shuffle - Switching Networks Kautsa, Stone, manipulators Fan, banyan network, shuffle register with cubes of memory [3].

IV. CONCLUSION

The current stage of development of computer systems, IBM AXES has a short but vivid history. It is characterized by the accumulation of quantitative theoretical and hardware and software organization of symbolic computing and knowledge processing. Intellectualization of calculations, i.e. transition from data processing to knowledge processing systems in the near future will lead to massive use of computer and telecommunications equipment to enhance the intellectual capabilities of man. The basis of a new class of computing, process-oriented analysis, understanding and synthesis of new knowledge will make their own circuit solutions, based on the basic elements of the future - the optics. Optical components and functional units will ask a variety of data due to reconfiguration and compression, as well as to parallel processing on nonspecific operations, while the spatial reconfiguration of the elements of the matrix, and associative processing fragments of characters in a smart storage devices.

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A Survey of Elliptic Curve Cryptography Implementation Approaches for Efficient Smart Card Processing

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Abstract - Smart cards have been used for many different purposes over the last two decades, from simple prepaid credit counter cards used in parking meters, to high security identity cards intended for national ID programs. This has increased data privacy and security requirements. Data protection and authentication is now demanded for performing Electronic payment and allow secure multi-level access to private information. ECC uses smaller key sizes compared to traditionally used RSA based cryptosystems. Elliptic Curve Cryptography is especially suited to smart card based message authentication because of its smaller memory and computational power requirements than public key cryptosystems. It is observed that the performance of ECC based approach is significantly better than RSA and DSA/DH based approaches because of the low memory and computational requirements, smaller key size, low power and timing consumptions.

Keywords : symbolic Elliptic Curve Cryptography, finite fields, smart cards, Biometrics.

GJCST Classification: C.3

A SURVEY OF ELLIPTIC CURVE CRYPTOGRAPHY IMPLEMENTATION APPROACHES FOR EFFICIENT SMART CARD PROCESSING

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Abstract - Smart cards have been used for many different purposes over the last two decades, from simple prepaid credit counter cards used in parking meters, to high security identity cards intended for national ID programs. This has increased data privacy and security requirements. Data protection and authentication is now demanded for performing Electronic payment and allow secure multi-level access to private information. ECC uses smaller key sizes compared to traditionally used RSA based cryptosystems. Elliptic Curve Cryptography is especially suited to smart card based message authentication because of its smaller memory and key computational power requirements than public cryptosystems. It is observed that the performance of ECC based approach is significantly better than RSA and DSA/DH based approaches because of the low memory and computational requirements, smaller key size, low power and timing consumptions.

Keywords : Elliptic Curve Cryptography, finite fields, smart cards, Biometrics.

I. INTRODUCTION

mart card is a credit-card sized plastic card with an embedded computer chip. Smart cards play an increasingly important role in everyday life. We encounter them as credit cards, loyalty cards, electronic purses, health cards, and as secure tokens for authentication or digital signatures. Their small size and the compatibility of their form make them ideal carriers of personal information such as secret keys, passwords, customization profiles, and medical emergency information. Electronic Payment is one of the most widely used applications of the smart card and is the most familiar among the average user. There are several different types of smart cards in this category, all of which deal with currency or a fiscal value. Smart cards can provide multi-factor authentication by using PIN/Biometrics combination with the card.

Multi-factor authentication approach is recommended in which security requirements are intended for highly secure installation and mandate a robust solution. Multi-factor authentication ensures

E-mail : jayabhaskarm@gmail.com , Author^a : Professor, Department of Computer Science & Technology Sri Krishnadevaraya University. Ananthapur, A.P. India. E-mail : bachalasatya@yahoo.com verification and validation of a user identity using multiple authentication mechanisms. It often combines two or more authentication methods-for example, a three-factor authentication is based on password (Something you know), smart card (Something you have), and fingerprints (Something you are). For example, in addition to what the user knows (such as a PIN), the card can provide authentication using the card owner's digital certificate with the card owner's public key. The digital certificate associates the card owner's identity to the person's public key. The smart card also contains the card owner's private key, which can be used for digitally signing e-mail or documents. With the support of biometric technologies, the smart card can also be used to store biometric templates of the card owner, which can be used to verify the card owner by acquiring a biometric sample (such as a fingerprint) and matching it to the reference template stored on the card or off the card using a biometric authentication server. Using biometric templates can be considered for security-sensitive applications where PINs can be stolen [1]. Unlike standard public-key methods that operate over integer fields, the elliptic curve cryptosystems operate over points on an elliptic curve. Cryptographic algorithms based on discrete logarithm problem can be efficiently implemented using elliptic curves [21]. ECC is emerging as an attractive public-key cryptosystem for smart cards because compared to traditional cryptosystems like RSA/DH, it offers equivalent security with smaller key sizes, faster computation, lower power consumption, as well as memory and bandwidth savings [2].

II. SMART CARD & ARCHITECTURE

Smart cards come in two varieties: memory and microprocessor. Memory cards simply store data and can be viewed as a small floppy disk with optional security. A microprocessor card, on the other hand, can add, delete and manipulate information in its memory on the card. Similar to a miniature computer, a microprocessor card has an input/output port operating system and hard disk with built-in security features.

a) Contact Vs. Contactless

Smart cards have two different types of interfaces: contact and contactless. Contact smart

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cards are inserted into a smart card reader, making physical contact with the reader. However, contactless smart cards have an antenna embedded inside the card that enables communication with the reader without physical contact. A combi card combines the two features with a very high level of security.

b) Basic Smart Card Chip Architecture

The basic smart card architecture is shown on Figure 1. It is a complete set of a microcontroller. It is a small embedded computer with low processing power (8-bit CPU, 5 MHz clock) and small memory (4 Kb RAM, 16 Kb EEPROM, 64 Kb ROM). It is secure and inexpensive [20].

databus	
	RAM
	EEPRON



Smart card components are:

CPU (Central Processing Unit): The heart of the chip, all computational work like implementing cryptographic algorithms and data exchange goes via this function.

Test Logic: A verification function only used during the production process to test all internal circuits for manufacturing faults.

Security Logic: A continuous function that checks environmental conditions that could jeopardise the security of the smart card.

I/O Interface: A communication function that takes care of receiving external commands and sending back responses using a serial communication protocol.

ROM: The permanent memory of the chip. It can contain parts of the operating system and self test procedures.

RAM: The CPU's scratch pad memory. This is used for storing temporary or intermediate data like session keys, internal variables and stack data.

EEPROM: Non-volatile updateable memory. It is used for storing application data like keys, PINs, balances, phone numbers, Biometric template and sometimes application or even operating system code.

Data Bus: The transfer channel within the chip. All information exchanged between the various functions passes through this channel.

III. BIOMETRIC AUTHENTICATION

technique Biometric is an automated methodology for the recognition of a person based on behavioral or physiological characteristics. These characteristics include features such as hand geometry, handwriting, face, fingerprints, vein, voice, retina, and iris. Biometric technologies are now the key to an extensive array of highly secured identification and personal verification solutions. Biometric system is a pattern recognition technology that makes personal identification of an individual by determining the authenticity of a specific physiological or behavioral characteristics possessed by the user [3].

a) Biometric Based Implementation on Smart Card

The use of biometrics within the card itself will mean that biometric features (fingerprint, retina, voice etc) can reliably identify a person. The use of some of these features has already been implemented in many applications. Table 1 below gives the required bytes for various biometric types. Additional information about biometric technology and standards can be found from the following organizations: The Biometric Consortium (www.biometrics.org), International Biometric Industry Association (www.ibia.rg), or BioAPI Consortium (www.iapi com) [4].

Biometric System	No. of Bytes Required
Finger scan	300-1200
Finger geometry	14
Hand geometry	9
Iris recognition	512
Voice verification	1500
Face recognition	500-1000
Signature verification	500-1000
Retina recognition	96

Table 1: No. of Bytes required for various Biometric systems

b) Classification of Biometric Approaches

Main Biometric based smart card implementation approaches are "match-off-card" and "match-on-card".

Match-off-card: For this type of implementation, the enrolled template is initially loaded onto the smart card and then transferred from the smart card via either contact or contactless interface when requested by the external biometric system. The external equipment then compares a new live template of the biometric with the

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one retrieved from the smart card. This implementation clearly has some security risks associated with transmitting the enrolled template off of the smart card for every biometric comparison. Appropriate security measures should be implemented to ensure the confidentiality and integrity of the released template.

Match-on-card: This implementation technique initially stores the enrolment template in the smart card's secure memory. When a biometric match is requested, the external equipment submits a new live template to the smart card. The smart card then performs the matching operation within its secure processor and securely communicates the result to the external equipment.

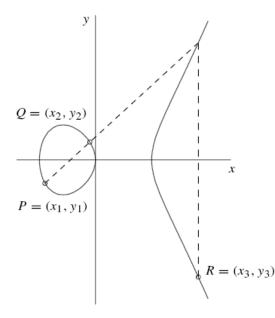
Biometric match-on-card approach can provide more private and secure identity verification system compare to match-off-card approach [5].

V. ELLIPTIC CURVE ARITHMETIC

Elliptic curves are not like an ellipse or curve in shape. They look similar to doughnuts. Geometrically speaking they somehow resemble the shape of torus, which is the product of two circles when projected in three-dimensional coordinates. ECC makes use of elliptic curves in which the variables and coefficients are restricted to elements of a finite field. There are two families of elliptic curves defined for use in cryptography: prime curves defined over odd prime field F_P and binary curves defined over Galois field $GF(2^m)$.

a) Geometrical Definition of Point Addition and point Doubling using chord-and-tangent rule

For any two points $P(x_1, y_1) \neq Q(x_2, y_2)$ on an elliptic curve, EC group law point addition can be defined geometrically (Figure 2) as: "If we draw a line through P and Q, this line will intersect the elliptic curve at a third point (-R). The reflection of this point about x-axis, $R(x_3, y_3)$ is the addition of P and Q".



For P=Q, point doubling, geometrically (Figure 3) if we draw a tangent line at point P, this line intersects elliptic curve at a point (-R). Then, R is the reflection of this point about x-axis.

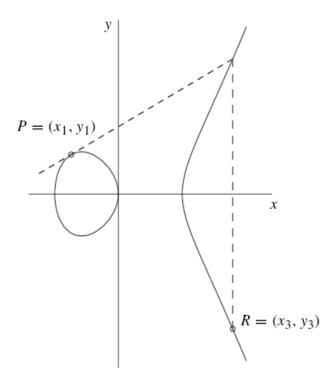


Fig.3 : Doubling: R=P+P

b) Point Multiplication

The dominant operation in ECC cryptographic schemes is point multiplication. This is the operation which is the key to the use of elliptic curves for asymmetric cryptography---the critical operation which is itself fairly simple, but whose inverse (the elliptic curve discrete logarithm) is very difficult. ECC arranges itself so that when you wish to performance operation the cryptosystem should make easy encrypting a message with the public key, decrypting it with the private key the operation you are performing is point multiplication. Scalar multiplication of a point P by a scalar k as being performed by repeated point addition and point doubling for example 7P = (2((2P)+P)+P).

c) Elliptic Curve Over F_P and F_2^m

Definition of elliptic curve over F_P as follows [6].

Let p be a prime in F_P and a, $b \in F_P$ such that

 $4a^3+27b^2\neq 0 \mbox{ mod } p$ in $F_P,$ then an elliptic curve E (F_P) is defined as

$$E \; (F_P) {:=} \; \{ \; p(\; x, \, y) \; , \, x, \, y \, \in \, F_P \; \}$$

Such that $y2 = x^3 + ax + b \mod p$ together with a point O, called the point at infinity. Below is the definition of addition of points P and Q on the elliptic curve E (F_P). Let P(x₁, y₁) and Q(x₂, y₂) then

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$$R=P+Q= \ \left\{ \begin{array}{cc} O & \mbox{ If } x_1=x_2 \mbox{ and } y_2=-y_1 \\ \\ Q=Q+P & \mbox{ If } P=O \\ \\ (x_3,y_3) & \mbox{ otherwise} \end{array} \right.$$

Where

$$x_{3} = \begin{cases} \lambda^{2} - x_{1} - x_{2} & \text{If } P \neq \pm Q \text{ (Point Addition)} \\ \\ \lambda^{2} - 2x_{1} & \text{If } P = Q \text{ (Point Doubling)} \end{cases}$$

 $y_3 = \lambda (x_1 - x_3) - y_1$, and

$$\lambda = \begin{cases} \frac{y_2 - y_1}{x_2 - x_1} & \text{If P} \neq \pm Q \text{ (Point Addition)} \\\\ \frac{3x_1^2 + a}{2y_1} & \text{If P} = Q \text{ (Point Doubling)} \end{cases}$$

The point p(x, -y) is said to be the negation of p(x, y).

The elliptic curves over F_2^m is defined as follows.

Denote the (non-super singular) elliptic curve over ${F_2}^m$ by E $({F_2}^m).$ If $a,b\in {F_2}^m$ such that $b\neq 0$ then

$$E(F_2^m) = \{p(x, y), x, y \in F_2^m\}$$

such that $y^2 + xy = x^3 + ax^2 + b \in F_P^m$ together with a point O, called the point at infinity.

The addition of points on E (F_2^m) is given as follows: Let $P(x_1, y_1)$ and $Q(x_2, y_2)$ be points on the elliptic curve $E(F_2^m)$, then

$$R = P + Q = \begin{cases} O & \text{ If } x_1 = x_2 \text{ and } y_2 = -y_1 \\ Q = Q + P & \text{ If } P = O \\ (x_3, y_3) & \text{ otherwise} \end{cases}$$

Where

$$x_{3} = \begin{cases} \lambda^{2} + \lambda + x_{2} + x_{1} + a & \text{If } P \neq \pm Q \text{ (Point Addition)} \\ \\ \lambda^{2} + \lambda + a & \text{If } P = Q \text{ (Point Doubling)} \end{cases}$$

 $y_3 = \lambda (x_1 + x_3) + x_3 + y_1$

and

$$\lambda = \begin{cases} \frac{y_2 + y_1}{x_2 + x_1} & \text{If } P \neq \pm Q \text{ (Point Addition)} \\ x_1 + \frac{x_1}{y_1} & \text{If } P = Q \text{ (Point Doubling)} \end{cases}$$

VI. ELLIPTIC CURVE CRYPTOGRAPHY FOR MESSAGE AUTHENTICATION

The use of Elliptic Curve Cryptography was initially suggested by Neal Koblitz [7] and Victor S. Miller [8]. Elliptic curve cryptosystems over finite field have some advantages like the key size can be much smaller compared to other cryptosystems like RSA, Diffie-Hellman since only exponential-time attack is known so far if the curve is carefully chosen [7] [6] and Elliptic Curve Cryptography relies on the difficulty of solving the Elliptic Curve Discrete Logarithm Problem ECDLP, which states that, "Given an elliptic curve E defined over a finite field F_P , a point $P \in E(F_P)$ of order n, and a point $Q \in E(F_P)$, find the integer $k \in [0, n-1]$ such that Q = k P. The integer k is called the discrete logarithm of Q to the base P, denoted $k = log_PQ$ ".

a) Elliptic Curve Encryption/Decryption

Consider a message 'Pm' sent from A to B. 'A' chooses a random positive integer 'k', a private key 'n_A' and generates the public key $P_A = n_A \times G$ and produces the cipher text 'Cm' consisting of pair of points $Cm = \{ kG, Pm + kP_B \}$ where G is the base point selected on the Elliptic Curve, $P_B = n_B \times G$ is the public key of B with private key 'n_B'.

To decrypt the cipher text, B multiplies the 1st point in the pair by B's secret & subtracts the result from the 2nd point $Pm + kP_B - n_B(kG) = Pm + k(n_BG) - n_B(kG) = Pm$.

VII. VARIOUS ECC IMPLEMENTATION APPROACHES ON SMART CARD

Ahmad Khaled M. AL-Kayali In [14] demonstrated the advantages and disadvantages of using Prime/binary fields to implement ECC on smart cards. Prime fields are best for software applications where as Binary fields are suitable for Hardware applications [22]. To access remote information systems Password authenticated key agreement scheme [15] is very useful in limited computation and communication resource (smart card) environments. A two-phase authentication mechanism proposed [16] by Abhilasha, Anna Squicciarini, Elisa Bertino. In that first phase consists of a two-factor biometric authentication and second phase combines several authentication factors in conjunction with biometric to provide a strong authentication. A key advantage of this approach is that any unanticipated combination of factors can be used. Disadvantage of using existing remote user authentication schemes [17] [18] is if the smart card is lost and password is revealed then any one can impersonate to sever as authorized user. To overcome this K K Goyal and M S Chahar proposed a new scheme [19] using Biometrics. Table 2 presents ECC based implementations on Smart Card applications.

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S.No	Implementation Approach	Implemented Field	Aim/Impact	Implemented smart card model
1	Smart cards do not require coprocessor to execute arithmetic operations of ECC but RSA/DSA need additional on chip hardware to avoid long processing delays [11].	Binary Field	Reduces the cost of Smart Card.	Intel 8051 microcontroller
2	ECC implementation, that relies on JAVA card technology and portable solution capable of running on PC and Smart card [12].	Binary Field	Implemented Efficient algorithms on low-resource smart cards.	Bull Odyssey I
3	Efficient implementation of the elliptic curve Digital Signature using optimized point addition and doubling algorithms when a crypto coprocessor for modular arithmetic is available [13].	Prime Field	ECDSA implementation is efficient compare with RSA and it has investigated curves over GF(p) because GF(2 ^m) field is used for efficient hardware implementation.	Motorola M- smart card Jupiter

Table 2 : ECC implementation details on Smart Card

a) Comparing ECC with other PKC Schemes

The majority of public key systems in use today use 1024-bit parameters for RSA and Diffie-Hellman. The US National Institute for Standards and Technology [NIST] has recommended that these 1024-bit systems are sufficient for use until 2010. Table 3 shows NIST guidelines on choosing computationally equivalent symmetric and public-key sizes [10].

Table 3 : Comparing ECC with other PKC schemes

Security(bits)	RSA key Length (bits)	ECC key Length (bits)	DSA/DH (bits)	Key Size Ratio of RSA and ECC	MIPS years to attack	Protection attack
80	1024	160-223	1024	1:6	10 ¹²	Until 2010
112	2048	224-255	2048	1:9	10 ²⁴	Until 2030
128	3072	256-383	3072	1:12	10 ²⁸	Bey-
192	7860	384-511	7860	1:20	10 ⁴⁷	ond 2031
256	15360	512+	15360	1:30	10 ⁶⁰	2031

ECC is the best suited in constrained environments. The advantages like speed and smaller keys or certificates are especially important in environments where at least one of the following resources is limited [9]: processing power, storage space, band width, or power consumption. This advantage is because its inverse operation gets harder, faster, against increasing key length than do the inverse operations in Diffie Hellman and RSA.

Table4: Measured performance of public-key algorithms

	ECC- 160	RSA- 1024	ECC- 192	RSA- 1536	ECC- 224	RSA- 2048
Ops/ sec	271.3	114	268.5	36.4	195.5	17.8
Performa- nce ratio	2.4:1		7.4 : 1		11.4 : 1	
Key-size ratio	1 : 6.4		1:8		1 : 9.1	

Table 4 shows a comparison of the RSA and ECC cryptographic operations performed by an SSL server. Open SSL speed program is used to measure RSA decryption and ECDH operation for different key sizes (a minor enhancement was made for collecting RSA-1536 numbers). These micro-benchmarks highlight ECC's performance advantage over RSA for different security levels. ECC's performance advantage increases even faster than its key-size advantage as security needs increase [10].

VIII. CONCLUSION

The smart card market has experienced a spectacular growth over the past few years. Along with their growing popularity there has been a corresponding growth of interest in their security. With respect to end-to-end security no other security solutions nearly as good and affordable as smart cards exist. Elliptic curve cryptography has been emerged as a vast field of interest for application specific security requirements. The elliptic curve discrete logarithm problem makes ECC most efficient compared to earlier RSA/DSA algorithms.

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LH-Cipher: A Linear Hierarchical Cipher approach for Data

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Abstract - Dividing the data into blocks and there by arranging the blocks in hierarchal order is termed as a linear hierarchical cipher approach for data .The encryption code, a access id code for each level based on the propagation code is generated in this technique. However the level propagation code and the access-id code of previous hierarchy level are matching with upper hierarchical level .The access-id code is set based on the time sensing key and a time seed, and the time seed updates with respect to the encryption pulse. By simply modifying and inversing the data security it is possible to decrease the number of key volume.

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LH-Cipher: A Linear Hierarchical Cipher approach for Data

M Sadanandam

Abstract - Dividing the data into blocks and there by arranging the blocks in hierarchal order is termed as a linear hierarchical cipher approach for data .The encryption code, a access id code for each level based on the propagation code is generated in this technique. However the level propagation code and the access-id code of previous hierarchy level are matching with upper hierarchical level .The access-id code is set based on the time sensing key and a time seed, and the time seed updates with respect to the encryption pulse. By simply modifying and inversing the data security it is possible to decrease the number of key volume.

I. INTRODUCTION

ith the growth in communication technology, there have been lot many positives but to counterfeit a lot many techniques to misuse this technology have also been growing. It is important to make data protected from all such malpractice [2]. Of all the methods of protecting the data, encryption is the most effective one. In this technique the data is simply hidden and later it is recovered by de-encryption.[1].to encrypt a data, a specific process or pattern is followed which may include mathematical operations, shifting and substitute techniques .after the data is encrypted it is termed as ciphertext [3].with the help of key based algorithms it is possible to encrypt the data and this key based encryption technique is classified as symmetric and asymmetric. The former uses only a single key for both encryption and de-encryption where as the later uses two different keys each for encryption and deencryption. There are number of key based Encryption techniques viz. DES, RSA, Elliptic curve, and several other mathematical methods [4, 5]. The wireless communication systems have seen a rapid development in recent times as such Wireless Sensor Network, Bluetooth, zigbee are the most recent ones. The WSN finds its application in monitoring systems especially security concerns.

The WSN constantly sends the information about the state of the object being monitored to the control room that enables collection of related information.

II. RELATED WORK

Multilevel cryptosystems saw a steady growth in recent times. The following are some of the proposed

multilevel encryption methods explored in table 1.

The models [1, 2] provide multi level ciphering but the final result so obtained is not generic and databases specific .Linear hierarchical cipher based data encryption and decryption is generic and considers the heterogynous in each level to overcome the drawbacks of the proposed AES and elliptical curve method [3,4,5].

III. LINEAR HIERARCHICAL CIPHER Approach

To ensure data protection in wireless communication encryption is the best technique but today we have lot many users accessing different levels of data. A multi user system has an access to different data levels. For each level we have an encrypted key which is used to de-encrypt and access it. However as the number of levels increases it get difficult to manage with the multiple keys .Hence forth managing the encryption with key technique is termed difficult. It is important to know that the keys are changed every time and hence data security is still ensured .Since the keys used are to be changed each time both level-based keys and time-based keys are to be altered each time.

To resolve the above problem of managing both the time based and level based keys at a time a data encryption technique is explained in detail. As stated earlier the data is initially partitioned into different level. While encrypting the data a specific level we also consider the encryption code of the previous level .Thus the user can de-encrypt the data easily from the already de-encrypted data levels however the data security still holds good. This technique of managing data at various levels is called a linear hierarchical cipher based encryption technique.

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Method	Proposed by	Special features
Multi level encryption	Zhou Yuping et al [1]	Encrypt the data system, table level and field level of
		objects
Multi level secondary storage	Chaitanya et al[2]	Flexible performance against security trade-offs
Multi level crpto disk(MLCD)		For generic storage devices
Multi level secure architecture	Sathiaseelan et al[3]	Integrated web services especially for academic institutions
Parallel AES algorithm	Deguang Le et al[4]	Fast Data Encryption on GPU to overcome the draw backs of CPU resource consumption.
ElGamal encryption and transmission scheme	5 Fu Minfeng et al[5]	It is based on elliptical based cryptosystem that aimed to improve ElGamal algorithm, ECC ElGamal encryption algorithm

Table 1 : Current State of the art in multi level encryption model

Firstly the data is divided into different levels and each level is related to at least one user. Then we encrypt each level by using the encryption key of each level based on the level propagation code and a access-id code of each level. However the level propagation code and the access-id code of one level are generated based on the level propagation code and the access-id code of previous level (the access-id code is produced based on the time propagation code and a time seed). The time seed has to be periodically altered. Then the encrypted data is transferred to the user. This method also includes the generation of encryption code for each level and also other authorized levels based on the level propagation and access id codes and then again decrypting the data at respective levels.

In the paper we elicit a new concept of linear hierarchical cipher based encryption considering a data storage and one encryption module. The data storage generates levels based on the different user approaching for the data access. This also produces the time propagation code, a time seed and a level propagation code based on the propagation codes of the previous levels. With the help of encryption key ,the encryption module encrypts the data with the help of time propagation code, the time seed, and the level propagation code of each level, and thus generates the access-id key (based on the accessed code of previous level) according to the time propagation key and the time seed. All the encrypted data is stored by the data storage. The decryption module finds the related encrypted data block in the related authorized level and then produces the encryption codes to access that level

data block with the help of level propagation codes and access id codes .It then decrypts the encrypted data blocks with the help of the propagation level and access id codes which could be generated with the help of previous level codes The data storage also considers the variation in the encryption code with the time with the help of time seed and thus generates the access-id code for each level considering the encryption periods .

IV. ENCRYPTION APPROACH

- > Divide data into multitude data blocks
- Generate access-id key of the highest level of the hierarchy
- Sequentially generate the access-id keys of the other levels of the hierarchy using FIPS-180-1 hash standard.
- Generate encryption key of the each hierarchical level based on level propagation and access-id keys of each level
- Encrypt the data block each level using corresponding encryption key
- Send encrypted data-block to data storage

V. DECRYPTION APPROACH

- Authenticate the user according to user key and find user position in hierarchy
- ➢ if authentication succeed then
 - generate an access-id key for the hierarchy level of the user
 - send encrypted data blocks to the authorized levels
 - Encryption time and access-id key to the

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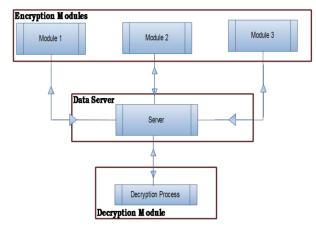
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decryption module.

- decryption module generates propagation keys for current level and other authorized levels of the hierarchy
- decryption module generates access-id keys of the authorized levels of the hierarchy by using access-id key of the current level
- Respectively decrypts the corresponding encrypted data-blocks according to the level propagation keys and access-id keys of the authorized levels by decryption module



VI. EMBLEMATIC MODEL OF LH-CIPHER

Figure 1 : schematic representation of LH-Cipher

Let us consider an Emblematic Model represented as fig 1, the linear hierarchical cipher system that includes three encryption modules, and data storage. In the selected emblematic model, the LH-Cipher implemented based on an ad hoc network. The considered Emblematic Model consist three nodes with encryption modules those serves as data collectors, and a control device that serves as data storage. In the Ad hoc network, the three nodes collect related data such as images around where they are disposed, and respectively encrypt the data through the first second and the third encryption modules and transmit the encrypted data to the control device that acts as data storage. Whenever required an eligible user can connect to the data storage through the network to read the data recorded therein. The operation of the Ad hoc Network is not in the context of proposal and therefore will not be discussed here. However, it should be understood that the LH-Cipher is not limited only to an ad hoc network; rather it may also be deployed in other constrained communication device environments, such as IEEE802.11 family standard device based networks.

The control device accepts the egress data of the encrypted modules as input and stores in data storage. A generic data access device with a database can be data storage.

As a part of its functionality the data storage controller groups the authorized users into multiple levels so as to manage these users and the data to be accessed by these users based on the users level. It is precise that a user of a level can access more data than a user of its lower levels. In other words, the users in upper level groups will have high end rights when compared to users in lower level groups. An upper level user can decrypt and access the data assigned to his group level and all lower group levels but it is not true in vice versa.

In order to manage the rights of preceding users of diverse levels, the data storage generates a level propagation key for each of the levels to encrypt the data. In particular, the level propagation key of a level is generated according to the level propagation key of its upper level so that the data can be managed based on the user levels.

Let consider that in the selected emblematic model, the data storage groups three different users u1, u2 and u3 into three levels, wherein the u1 belongs to the first level which has the highest right, the u2 belongs to a second level which has the second highest right, and the u3 belongs to a third level which has the lowest right.

The data storage randomly generate a group key $K_{(g)1}$ for the users of the first level that also considered as top level and then sequentially generates group keys $K_{(g)2}$, $K_{(g)3}$ for the other two levels through a fips-180-1 standard hashing technique, as shown below:

$$K_{(g)i} = F(h)^{l-1} (K_{(g)1}),$$

Where in F(h) is a hash function and I represents the level (i.e. 1=1..3). The Eq1 represents the FIPS-180-1 standard hashing function that is using to generate the group keys $K_{(g)2}$ and $K_{(g)3}$ of the other two level.

Next, the data storage respectively generates a level propagation key $\{PK_{(m,l)}, `m' \text{ is node identification code and `l' is level id.} for each level according to the group keys <math display="inline">\{K_{(g)1}, K_{(g)2}, K_{(g)3} \ldots K_{(g)n}\}$ of the levels and an identification code of the encryption module through the fallowing function.

$$PK_{(m,l)} = f_{(e)}(K_l, m),$$

Wherein $f_{\rm (e)}$ is the encryption function, and I represents the level id.

In the selected emblematic model, the encryption function is any standard encryption function of choice, as a part experimental results we opt to the advanced encryption standard (AES).

It should be mentioned that in the selected emblematic model, the node identification codes are used as one of the factors for generating the level propagation keys because a different level propagation key is provided to each of the node in selected network. However, if the situation of multiple nodes is not January 2012

considered or every node uses the same level propagation then as an alternative the group key can be used as the level propagation key.

The data storage also generates a access-id key(A_k) and a time seed besides the level propagation keys. The access-id key(A_k) and the access time as seed are used for generating an access identification key(A_k) for each encryption period. In the selected emblematic model, a different access identification key(A_k) is used during each encryption period so that the data to be encrypted can have forward and backward data security. Therefore, a user with expired authorization unable to use his original key to access the data, and can avoid a new authoritative user from accessing data that encrypted in past.

For example, the data storage generates the access-id key{ $A_{(k)m}$, m is device id} by using a primary key $K_{(p)}$ and an identification code of the wireless sensor through a sixth function. In the selected emblematic model, the encryption function used as shown below:

$$A_{(k)m} = f_{(e)}(K_{(p)}, m),$$

Wherein $f_{\rm (e)}$ is the encryption function. In the selected emblematic model, the encryption function is an standard model of our choice.

Similarly, in the selected emblematic model, the identification codes of the nodes involved are used as one of the factors for generating the access-id key(A_k) because a different access-id key(A_k) is provided to each node involved. However, if the situation of multiple nodes is not considered or each of the nodes uses the same access-id key(A_k), the primary key $K_{(p)}$ can be directly used as the access-id key.

The data storage generates a user key for each of the users and assigns the user key $K_{(u)m}$ to the user while assigning the group key to the user. This user key will be generated with the help of fallowing equation represents an encryption function.

 $K_{(u)m} = f_{(e)}(K_{(p)},m)$, wherein m is user identification.

 $K_{(u)m}$ is user key for user identified by m.

 $f_{(e)}$ is any encryption function of choice

K_(p) is primary key

m is user identification

The primary key $K_{(p)}$ of the data storage is generated randomly. Besides, the data storage generates a different access identification seed $S_{\rm T}$ corresponding to different encryption periods T. In the selected emblematic model, the $S_{\rm T}$ corresponding to the current encryption period is generated according to the $K_{(p)}$ and the other parameter of choice such as current date or timestamp.

As described above, all encryption modules are used for encrypting the data to be transmitted by

corresponding nodes. The process of encryption fallows.

The first encryption module receives the access-id key(A_k), the time seed S_T , and the level propagation key { $K_{(L)I}$, 1 is level identifier} of each level from the data storage, wherein I represents the level. In the selected emblematic model, the data storage broadcasts a new time seed S_T at certain intervals to the all encryption modules to allow them to generate the access identification keys of the current encryption period T according to the new time seed and the access-id key. Access Identification Key can be generated using the fallowing function.

 $AI_{k(m,T)} = f_{(h)} (A_{K(m)}, S_T)$, wherein $f_{(h)}$ is the hash function.

The process of AI_k generation is sequential, that is the first encryption module generates the access identification key of the second level according to the access identification key of the first level and finally generates the access identification key of the third level according to the access identification $key(AI_k)$ of the second level.

The first encryption module divides a data to be transmitted into multitude of sub-data blocks corresponding to different user levels. In addition, the first encryption module generates an encryption key for each level according to the received level propagation key of the level and the access identification key(AIk) generated based on a new seed. Encryption key will be generated by using the fallowing function.

$$K_{(E) (m,l,t)} = f_{(h)} (K_{L(m,l)}, f_{(h)}^{l-1} (AI_{k (m,t)})),$$

Wherein $f_{(h)}$ is the hash function, and I represents the level and m represents the node id.

The first encryption module uses the encryption key $K_{(E)(1,L,T)}$ (wherein 1 is first node id and L=1..3) of each level for respectively encrypting the sub-data blocks.

If the first encryption module does not receive the new time seed but generates the access identification $key(AI_k)$ by using the old time seed and encrypts the sub- data blocks by using the encryption key generated by using the old access identification key, the data storage determines the time seed after it receives the encrypted sub-data blocks and records the sub-data blocks which are encrypted by using the incorrect time seed as reference for subsequent data decryption. In addition, the data storage broadcasts the current time seed to the first encryption module again if the first encryption module does not use the correct time seed to encrypt the data.

After the encryption modules encrypt the subdata blocks and the encrypted data is sent to the data storage, the respective users can read the encrypted sub-data blocks stored in the data storage through the decryption module allotted to respective end user device. In the selected emblematic model, the end-user

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device is connected to the control device with the choice of network model; here we consider a wired connectivity.

The decryption module reads the encrypted sub-data blocks corresponding to the level of a user and other authorized levels of the user and corresponding to the encryption period from the data storage. To be specific, in the selected emblematic model, a user having higher right can read the data assigned to users having lower rights but a user having lower right cannot read the data assigned to users having higher rights. Thus, the data storage provides the corresponding authorized data to a user according to the level of the user after it authenticates the user according to a user key of the user.

In the selected emblematic model, the data storage generates a access identification $key(AI_k)$ corresponding to the level of the user and sends AI_k together with the encrypted sub-data blocks to the decryption module of the end-user device.

The decryption module generates the encryption cipher keys for the authorized levels (i.e., the second level and the third level) of the user according to the level propagation keys and the access identification keys of the authorized levels and decrypts the encrypted sub-data blocks by using the encryption keys. In particular, the decryption module generates the level propagation key and AI_k of a lower level according to the level propagation key and the access identification key(AIk) of an upper level.

VII. CONCLUSION AND FUTURE WORK

The proposed linear hierarchical ciphering model is robust and scalable where data is encrypted corresponding to multiple levels so that a user having higher right can access the data assigned to users having lower rights but a user having lower right cannot access data assigned to users having higher rights. In addition, in the present invention, an access identification key updated by using a access seeds generated based on access time is adopted to ensure the encrypted data to have forward and backward security and that no synchronous process is required. Thus, the encryption process relaxed from computational complexity. Moreover, the level propagation key and the access identification $key(AI_k)$ of a lower level are generated according to the level propagation key and the access identification $key(AI_k)$ of one level up in hierarchy. Thereby, the number of keys to be managed by an end-user device is reduced and accordingly the calculation load of the end-user device is also reduced. In future this solution can be extended to achieve the key generations without considering the sequence in levels of hierarchy.

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Performance Analysis of Stock Price Prediction using Artificial Neural Network

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Abstract - Stock market predictions are one of the challenging tasks for financial investors across the globe. This challenge is due to the uncertainty and volatility of the stock prices in the market. Due to technology and globalization of business and financial markets it is important to predict the stock prices more quickly and accurately. Last few years there has been much improvement in the field of Neural Network (NN) applications in business and financial markets. Artificial Neural Network (ANN) methods are mostly implemented and play a vital role in decision making for stock market predictions. Multi Layer Perceptron (MLP) architecture with back propagation algorithm has the ability to predict tools are used to predict the future stock prices and their performance statistics will be evaluated. This would help the investor to analyze better in business decisions such as buy or sell a stock.

Keywords : Artificial Neural Network (ANN), Multi Layer Perceptron (MLP), National Stock Exchange (NSE), Stock Prediction, Performance Measures.

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Performance Analysis of Stock Price Prediction using Artificial Neural Network

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Abstract - Stock market predictions are one of the challenging tasks for financial investors across the globe. This challenge is due to the uncertainty and volatility of the stock prices in the market. Due to technology and globalization of business and financial markets it is important to predict the stock prices more quickly and accurately. Last few years there has been much improvement in the field of Neural Network (NN) applications in business and financial markets. Artificial Neural Network (ANN) methods are mostly implemented and play a vital role in decision making for stock market predictions. Multi Layer Perceptron (MLP) architecture with back propagation algorithm has the ability to predict with greater accuracy than other neural network algorithms. In this research, neural works predict tools are used to predict the future stock prices and their performance statistics will be evaluated. This would help the investor to analyze better in business decisions such as buy or sell a stock.

Keywords : Artificial Neural Network (ANN), Multi Layer Perceptron (MLP), National Stock Exchange (NSE), Stock Prediction, Performance Measures.

I. INTRODUCTION

tock price prediction is a heated topic in prediction study of financial area. The use of ANN in business environments has been increasing over the last few years. Excellent algorithm has been applied to predict stock price or index. Interest in neural networks has led to a considerable surge in research activities in the past decade. Artificial neural network models are based on the neural structure of the brain. The brain learns from experience and so do artificial neural networks. As a useful analytical tool, ANN is widely applied in analyzing the business data stored in database or data warehouse. Identifying customer behavior patterns and predicting stock price are emerging areas of neural network research and its application. Most of the companies have created new methods of evaluating financial data and investment decisions. Artificial Neural Networks are being used by most companies for improved forecasting capabilities in analysis of stock market. So, artificial neural network suits better than other models in predicting the stock market.

To predict stock prices there are so many conventional techniques can be used, in which

fundamental and technical analysis one among them (Atiya, A. F, El-Shoura et al, 1999). Fundamental analysis involves various macro-economic factors, results of the company, financial conditions and other related attributes are used to measure the value of the company with reflect to stock price changes. Technical analysis, on the other hand, involves analyzing statistics generated by market activity, such as past prices and volume (Kai Keng Ang and Chai Quek, 2006). Recent development in soft computing has set a new dimension in the field of financial forecasting. Tools based on ANN have gained more popularity due to their inherent capabilities to approximate any non linear function to a high degree of accuracy.

The idea of forecasting using neural network is to find an approximation of mapping between the input and output data through training. The trained neural network is then used to predict the values for the future (Abhyankar, A. et al, 1997). This research work presents the use of artificial neural network as a forecasting tool for predicting the stock market price.

The remainder of the paper is organized as follows. Section II reviews the background study of the stock market prediction by Artificial Neural Network. Section III focuses on the objectives of the study. Section IV discusses about the basic of Artificial Neural Network; benefits and limitations of ANN were presented. Section V discusses about the Indian Stock market. Section VI explains about data and methodology of using NeuralWorks Predict to predict the stock prices and calculating result performance. Section VII concludes the research.

II. BACKGROUND STUDY

In the last two decades lot of research has been done on models based on intelligent soft computing. In general, the approaches to predict stock market could be classified into two classes, fundamental analysis and technical analysis (Kai Keng Ang and Chai Quek, 2006). Fundamental analysis is based on macroeconomic data and the basic financial status of companies like money supply, interest rate, inflationary rates, dividend yields, earnings yield, cash flow yield, book to market ratio, price-earnings ratio, lagged returns (Fama and French, 1988; Lakonishok, 1994). Technical analysis is based on the rationale that history will repeat itself and that and the correlation between price and volume reveals market behavior. Prediction is made by exploiting implications

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hidden in past trading activities and by analyzing patterns and trends shown in price and volume charts (Smirlock and Starks, 1985; Brush 1986).

According to (Refenes, Zapranis and Franchis, 1994) "neural networks are capable of making better prediction in capturing the structural relationship between a stock's performance and its determinant factors more accurately than MLR models". (Kryzanowski, Galler and Wright, 1993) using Boltzmann machine trained an artificial neural network with 149 test cases of positive (rise in the stock price) and negative (fall in the stock price) returns for the years 1987-1989 and compared this to training the network with positive, neutral (unchanged stock price), and negative returns for the same 149 test cases for the years 1987-1989. The network predicted 72% correct results with positive and negative returns. However the network predicted only 46% correct results with positive, neutral, and negative returns.

Using neural networks to predict financial markets has been an active research area in both fundamental and technical analysis, since the late 1980s (White, 1988; Fishman, Barr and Loick, 1991; Shih, 1991; Utans and Moody, 1991; Katz, 1992; Kean, 1992; Swales and Yoon, 1992; Wong, 1992; Azoff, 1994; Rogers and Vemuri, 1994; Ruggerio, 1994; Baestaens, Van Den Breg and Vaudrey, 1995; Ward and Sherald, 1995; Gately, 1996; Refenes Abu-Mostafa and Moody, 1996; Murphy, 1999; Qi, 1999; Virili and Reisleben, 2000; Yao and Tan, 2001; Pan, 2003a; Pan 2003b).

Fujitsu a Japanese technology company and Nikko Securities - an investment company joined together to develop a stock market prediction system for TOPIX (Tokyo based stock index). The emergence of artificial intelligence techniques has seen their enormous application to financial forecasting, such as expert systems (Tsaih Yenshan Hsu, and Charles Lai, 1998), fuzzy logic (Hiemstra, 1994), and neural networks (Kryzanowski, Galler and Wright, 1993). Among them, neural networks are the most popular and successful tools. There is extensive literature about the application of neural networks in financial forecasting (Azoff, 1994; Goonatilake and Treleaven, 1995; Wong and Selvi, 1998). One of the most popular Journals published on the application of neural networks in finance is the Journal of Computational Intelligence in Finance (Bhagirathi Nayak, et al, 2011).

Also, all of the researches using neural network applications in prediction of stock market trend are mainly based on the assumption that the basic laws in a certain stock market is consistent through the time of experiment data.

III. OBJECTIVES OF THE STUDY

The main objective of this study is to use NeuralWorks Predict tool to obtain more accurate stock prediction price and to evaluate them with some performance measures. This study can be used to reduce the error proportion in predicting the future stock prices. It increases the chances for the investors to predict the prices more accurately by reducing error percentage and thus gain benefits in share markets.

IV. ARTIFICIAL NEURAL NETWORK

Artificial Neural Network (ANN) is an information processing system where the elements called neurons, process the information. The signals are transmitted by means of connection links. The links possess an associated weight, which is multiplied along with the incoming signal (net input) for any typical neural network. The output signal is obtained by applying activations to the net input. The network consists of a set of sensory units that constitute the input layer and one or more hidden layer of computation modes. The input signal passes through the network in the forward direction. This type of network is called as multilayer perceptron (MLP) (Sivanandam, S.N. et al. 2006). The multilayer perceptron are used with supervised learning and have to lead the successful back propagation algorithm where logistic sigmoid function is widely used. The MLP network has hidden neurons and this will make the network more active for complex tasks. The layers of network are connected by synaptic weights and have a high computational efficiency.

a) Benefits of Using Artificial Neural Network

Neural networks often lead to significant results, e.g. in weather forecasting, a rule of weather change is less probable than a steady weather pattern. According to (Schoneburg, 1990), this is also true for stock prices.

A key aspect to successful forecasting lies in the ability to merge data available in diverse formats (Steven H. Kim and Se Hak Chun, 1998). The data analysis performed by neural networks tolerates a considerable amount of imprecise and incomplete input data due to the distributed mode of information processing. Neural network lie in their ability to predict accurately even in situations with uncertain data, and the possible combinations with other methods. Despite the benefits of artificial neural networks, there are still some limitations to neural networks that are discussed below.

b) Limitations of Artificial Neural Network

Some methods are executed with insufficient reliability tests, data design and with inability to identify the optimal topology for a specific problem domain.

There is no known method of designing an optimal neural network, but the best network is highly dependent on the data and application (Carlos Cinca. 1996).

Some of the limitations are mentioned below:

- 1. NN require very large number of previous data.
- 2. The best NN architecture topology is still unknown.
- 3. For complex networks the result and accuracy may

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decrease.

- 4. Statistical relevance of the result is needed.
- 5. More careful data design is needed and systematically analyzed.

In order to improve the NN applications, there are some other limitations, concerning the problems of evaluation and implementation of NN that should be discussed. Large number of research is done and implemented by companies that are not published in scientific indexes.

v. Indian Stock Market

Investors are mostly preferred the stock market investments because it has the opportunity of highest return over other schemes. For companies, stock market is one of the key sources to raise money through initial public offer (IPO). This allows businesses to be publicly traded, or raise additional capital for expansion by selling shares of ownership of the company in a public market. Indian stock market is mainly consists of two major stock indices, Bombay Stock Exchange (BSE) and National Stock Exchange (NSE). The benchmark for these two exchanges are Sensex (30 Stocks) and Nifty (50 Stocks).

BSE was the first stock exchange in the country and approved under the Securities Contract Regulation Act, 1956. Sensex is an index of 30 stocks with 12 major sectors. In the year 1993, National Stock Exchange of India has been the frontier of Indian securities market. NSE is located at Mumbai, India referred as Nifty. Nifty is a well diversified index consisting of 50 major stocks from 21 sectors of the economy (Refer NSE, 2010). It is the largest stock exchange in India in terms of daily turnover and number of trades, for both equities and derivative trading. Trading on both exchanges is carried out in dematerialized form.

Securities and Exchange Board of India (SEBI) is the regulatory authority and have the rights to monitor all the stock markets in India established by Government of India in the year 1988. The main goal of the board is to protect the investors in securities and regulate the stock market. There are 23 stock exchanges in India, out of that only 18 stock exchanges are currently in the operative mode. Among 18 exchanges BSE and NSE are considered to be the primary exchanges of India.

VI. DATA AND METHODOLOGY

The actual problem discussed in this paper is to forecast the stock price of National Stock Exchange in India. For this purpose we have used available daily stock data of TCS (i.e., bhavcopy) from the National Stock Exchange beginning from 01-November-2009 to 12-December-2011 (Refer NSE, 2011).

For this study, we select 508 day's NSE stock data of TCS Company. The data field used in this research consists of previous close, open price, high price, low price and close price. In order to predict the stock price, past data is necessary and it has been collected for the trading days from 01-November-2009 to 12-December-2011. The historical data set is available on the National Stock Exchange website.

The main task is to predict the stock price of TCS will be up or down for tomorrow by using the historical values of the company stock. In this research, NeuralWorks Predict version 3.24 packages are applied to predict the future stock price of TCS The historic data of previous close, open price, high price, low price and closing price data is used. NeuralWorks Predict 3.24 tool is used throughout the process, this research choose 5 important attributes including previous close, open price, high price, low price and closing price. The performance of the neural network largely depends on the architecture of the neural network. Issues critical to the neural network modeling like selection of input variables, data pre-processing technique, network architecture design and performance measuring statistics should be considered carefully.

a) Methodology of Building a Predict Model

General steps of building and predicting the value by using Multi Layer Perceptron model in the NeuralWorks Predict.

- 1. Building a Predict Model: To make predictions from data if target outputs can be any value in a continuous range of numeric values or a discrete ordered range of numeric values.
- 2. Selection of model: Multi Layer Perceptron (MLP) model is selected to predict the stock value.
- 3. MLP Input training data
- 4. MLP Output training data
- 5. MLP Training data characteristics
- 6. MLP Network parameters
- 7. Reviewing parameters and training the model
- 8. Saving the model
- 9. Training statistics
- 10. Testing a predict model
- 11. Specifying data sets for testing
- 12. Interpreting test results
- 13. Running a MLP predict model

b) Results and Performance Statistics

Performance statistics that are computed for prediction model train and test sets are shown in Table1.

Close Price	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Accuracy (20%)	Conf. Interval (95%)	Records
All	0.9971	0.995268	9.872307	49.1059	12.68418	1	24.72697	508
Train	0.997092	0.995172	9.816971	49.1059	12.68724	1	24.76381	355
Test	0.99713	0.995504	10.0007	37.47821	12.67708	1	24.87982	153

Table. 1 : Train and Test Results of TCS

In Table 1, R Correlation (R) is the linear correlation between predicted outputs and target

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outputs, in problem domain units. Average Absolute Error (Avg Abs) denotes the average absolute difference between predicted output values and target output values. Maximum Absolute Error (Max Abs) is the maximum absolute difference between a predicted output value and a target output value. The Root Mean Square Error (RMS) is the error between the predicted outputs and the target outputs. Accuracy is the percent of predicted output values that lie within 20% of their corresponding target output values. Confidence Intervals (Conf Interval) 95% of the model predictions lie within the range around target output values bounded by the confidence intervals and number of records processed. Finally, records indicate the number of records processed during training or testing.

The close correlation between the market value predicted by the neural network and the true value suggests that such networks may indeed become very powerful tools in financial applications. In this study, a real world output range is calculated whose limits are the minimum and maximum of all real world targets and real world model outputs. This range is used in several of the analysis results as shown in the Table 2.

Close Price	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Accuracy (20%)	Conf. Interval (95%)	Records
All	0.9971	0.995268	9.872307	49.1059	12.68418	1	24.72697	508
Primary	0.9971	0.995268	9.872307	49.1059	12.68418	1	24.72697	508
Secondary	0.9971	0.995268	9.872307	49.1059	12.68418	1	24.72697	508
Train	0.997092	0.995172	9.816971	49.1059	12.68724	1	24.76381	355
Test	0.99713	0.995504	10.0007	37.47821	12.67708	1	24.87982	153
Valid	0.9971	0.995268	9.872307	49.1059	12.68418	1	24.72697	508

Table.2 : Results Interpretation and Performance Statistics

c) Output Summary

The result of the predicted value has been shown in the Fig. 1. In Fig. 1, the actual close price of TCS have compared with the predicted price. Here, days refer to 508 values for each day starting from (19-November-2008 to 14-December-2010).

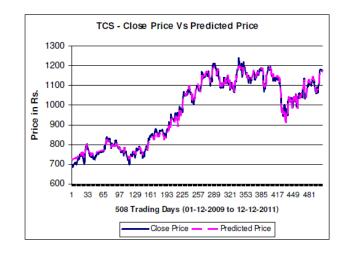


Fig. 1 : Comparison of Actual Vs Predicted Price

The error percentage rate of the actual close price and predicted price of TCS as shown in Fig. 2.

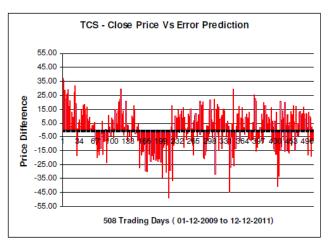


Fig.2 : Error Percentage rate of Actual Close Price Vs Predicted Price

There are many different methods to measure performance of systems. In order to evaluate the net performance of the stock value some of the following indicators have to be considered.

The indicators are R, Net-R, Average Absolute, RMS, Accuracy measures and Confidence limits. Net-R measure is the linear correlation between the real world target output and the raw neural net output. RMSE is a basic measure is used to find out the difference between values predicted by a model and the value actually observed.

We have used NeuralWorks Predict package tool for training, testing and predicting the stock prices. It is found that the percentage of correct prediction has been made and the result of this analysis is shown in the Table 2.

The train and test data sets are selected from the primary and secondary working sets, which will preliminary, trim the data sets. The following are the outcomes of all test and train set data.

All	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.9971	0.995268	9.872307	49.1059	12.68418	24.72697
2	0.93295	0.933051	20.20458	37.47821	21.68696	43.78338
3	0.920938	0.920919	5.275382	18.66345	6.604043	13.04181
4	0.932548	0.932497	10.21521	27.56531	11.92409	24.05034
5	0.839133	0.839492	16.69528	30.37744	17.85807	36.75226
6	0.788178	0.787327	13.77196	49.1059	17.27659	35.55556
7	0.805987	0.808338	11.81858	40.94519	15.69253	32.45215
8	0.851505	0.853173	9.347101	33.25348	11.24513	22.4312
9	0.904238	0.904655	9.318429	25.60205	11.07237	21.91745
10	0.85448	0.859078	6.983322	31.7677	9.281714	18.3149
11	0.613367	0.615451	11.67487	44.85999	15.94526	32.97479

Table.3 : Test and Train of all Working Set Data

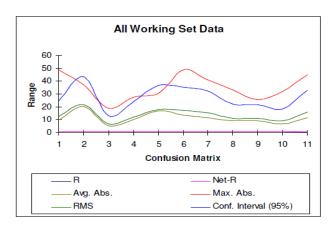


Fig.3 : Graph of all Working set Data

Primary	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.9971	0.995268	9.872307	49.1059	12.68418	24.72697
2	0.93295	0.933051	20.20458	37.47821	21.68696	43.78338
3	0.920938	0.920919	5.275382	18.66345	6.604043	13.04181
4	0.932548	0.932497	10.21521	27.56531	11.92409	24.05034
5	0.839133	0.839492	16.69528	30.37744	17.85807	36.75226
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10	0.85448	0.859078	6.983322	31.7677	9.281714	18.3149
11	0.613367	0.615451	11.67487	44.85999	15.94526	32.97479

Table.4 : Interpretation of Primary Working Set Data

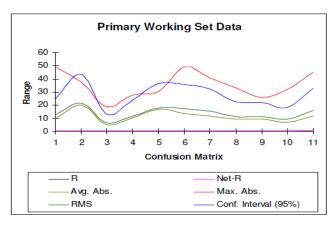


Fig.4 : Graph of Primary Working Set Data

Secondary	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.9971	0.995268	9.872307	49.1059	12.68418	24.72697
2	0.939141	0.93923	20.14999	36.18195	21.49552	44.14341
3	0.916674	0.91667	5.283045	18.66345	6.705967	13.33403
4	0.947125	0.947079	9.806209	23.55493	11.26785	23.0938
5	0.818945	0.819175	17.88929	30.37744	19.00811	40.15015
6	0.808768	0.809089	12.9965	49.1059	16.87607	35.487
7	0.780828	0.783043	13.4198	40.94519	17.92326	38.2675
8	0.883901	0.886189	9.083319	33.25348	10.99159	22.16953
9	0.899728	0.89992	9.392587	25.60205	11.10217	22.14604
10	0.874985	0.878135	6.668418	21.67969	8.546409	16.97648
11	0.730967	0.732311	11.01076	44.85999	15.92176	33.80093

Table.5 : Interpretation of Secondary Working Set Data

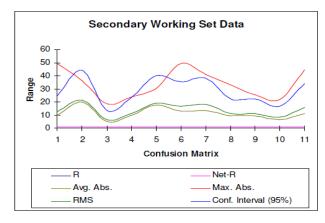
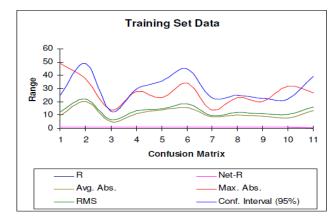


Fig.5 : Graph of Secondary Working Set Data

Train	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.997092	0.995172	9.816971	49.1059	12.68724	24.76381
2	0.921331	0.921463	20.32742	37.47821	22.11164	48.79345
3	0.932825	0.932779	5.257592	13.69232	6.361141	13.03735
4	0.898039	0.897986	11.16954	27.56531	13.33025	29.41568
5	0.909343	0.909957	14.00878	23.06158	14.95047	35.41038
6	0.752458	0.749573	15.87678	34.11078	18.31965	44.84638
7	0.935137	0.936032	8.616142	13.91046	9.812246	23.24043
8	0.762268	0.761667	9.967763	22.94531	11.82026	25.09368
9	0.91631	0.917143	9.142303	20.4364	11.00127	22.75063
10	0.821826	0.828276	7.697107	31.7677	10.76402	21.98249
11	0.437907	0.440396	13.28768	26.85999	16.00216	39.1732

Table.6 : Interpretation of Training Set Data





Test	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.99713	0.995504	10.0007	37.47821	12.67708	24.87982
2	0.93295	0.933051	20.20458	37.47821	21.68696	43.78338
3	0.920938	0.920919	5.275382	18.66345	6.604043	13.04181
4	0.932548	0.932497	10.21521	27.56531	11.92409	24.05034
5	0.839133	0.839492	16.69528	30.37744	17.85807	36.75226
6	0.788178	0.787327	13.77196	49.1059	17.27659	35.55556
7	0.805987	0.808338	11.81858	40.94519	15.69253	32.45215
8	0.851505	0.853173	9.347101	33.25348	11.24513	22.4312
9	0.904238	0.904655	9.318429	25.60205	11.07237	21.91745
10	0.85448	0.859078	6.983322	31.7677	9.281714	18.3149
11	0.613367	0.615451	11.67487	44.85999	15.94526	32.97479

Table.7 : Interpretation of Test Set Data

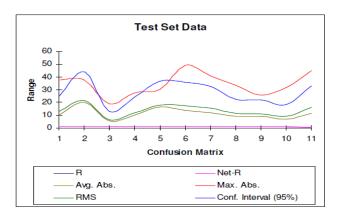


Fig. 7 : Graph of Test Set Data

Close Price	R	Net-R	Avg. Abs.	Max. Abs.	RMS	Conf. Interval (95%)
1	0.93295	0.933051	20.20458	37.47821	21.68696	43.78338
2	0.920938	0.920919	5.275382	18.66345	6.604043	13.04181
3	0.932548	0.932497	10.21521	27.56531	11.92409	24.05034
4	0.839133	0.839492	16.69528	30.37744	17.85807	36.75226
5	0.788178	0.787327	13.77196	49.1059	17.27659	35.55556
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8	0.904238	0.904655	9.318429	25.60205	11.07237	21.91745
9	0.85448	0.859078	6.983322	31.7677	9.281714	18.3149
10	0.613367	0.615451	11.67487	44.85999	15.94526	32.97479

Table.8 : Interpretation of Predicted Price

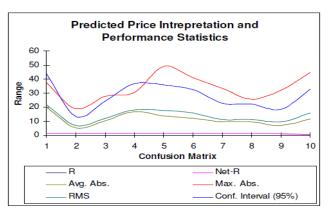


Fig.8 : Interpretation of Predicted Price and Performance Statistics

CONCLUSION VIII.

In this research, we examined and applied multilayer perceptron model by using the NeuralWorks Predict tool. The results from analysis shows that NeuralWorks Predict offer the ability to predict the stock prices more accurately than the other existing tools and techniques. The accuracy of the predicted output values that lie within 20% of their corresponding target output value. By using this tool one can have the ability to forecast the stock price of NSE more accurately. This analysis can be used to reduce the error percentage in

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predicting the future stock prices. It increases the chances for the investors to predict the prices more accurately by reducing the error percentage and hence increase their profit in share markets. Utilizing neural network models together with other forecasting tools and techniques can be considered yet another valuable advancement in the age of technology.

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Intentional Software Product Line

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Abstract - Software product line engineering optimizes the development of individual systems by leveraging their common characteristics and managing their differences in a systematic way. These differences are called variabilities. We argue that it is difficult for business people to fully benefit of the SPL if it remains at the software level. The paper proposes a move towards a description of software product line in intentional terms, i.e. intentions and strategies to achieve business goals. We present ISPL, the model to describe intentional Software Product Line. Thereafter, we propose our process to show how to use this model.

Keywords : Software Product Line, variability, intentional level, comparison framework, features modeling and metamodels.

GJCST Classification: D.4.6,



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Intentional Software Product Line

Sami Ouali^α, Naoufel Kraiem^Ω, Henda Ben Ghezala^β

Abstract - Software product line engineering optimizes the development of individual systems by leveraging their common characteristics and managing their differences in a systematic way. These differences are called variabilities. We argue that it is difficult for business people to fully benefit of the SPL if it remains at the software level. The paper proposes a move towards a description of software product line in intentional terms, i.e. intentions and strategies to achieve business goals. We present ISPL, the model to describe intentional Software Product Line. Thereafter, we propose our process to show how to use this model.

Keywords : Software Product Line, variability, intentional level, comparison framework, features modeling and metamodels.

I. INTRODUCTION

noftware product line engineering optimizes the development of individual systems by leveraging Utheir common characteristics and managing their differences in a systematic way (Clements & Northrop, 2001). These differences are called variabilities. In software product line engineering, two kinds of variability can be distinguished: product line variability and Software variability. Software variability refers to the ability of a software system to be efficiently extended, changed, customized or configured for use in a particular context (Svahnberg et al., 2005). While product line variability describes the variation between the systems that belong to a product line (Coplien et al., 1998; Pohl et al., 2005; Kang et al., 2002) in terms of properties and qualities, like features that are provided or requirements that are fulfilled. Defining product line variability concerns the determination of what should vary between the systems in a product line. In SPLE, single system can be built rapidly from reusable assets, such as a set of components.

The framework analysis which we proposed in our previous work (Ouali et al., 2011) allows us to identify many drawbacks of existing SPL construction methods. In these methods, apart requirement approaches ones, the problem is the matching between users' needs and the product offered by developers. Many writers have observed that there is a "conceptual mismatch" (Woodfield, 1997; Kaabi, 2007). The position adopted in this paper is to suggest a move to intentiondriven SPL to bridge the gap between high level users' goals and low level software product line obtained. We present in this paper a model for intentional SPL modeling.

Our process is based on goal modeling, feature modeling and metamodels. Goal models model stakeholder intentions to fulfill the system-to-be. Feature modeling allows us to model the common and variable properties of product-line members throughout all stages of product-line engineering. Metamodels allow the expression of common and variable characteristics of a set of applications. A metamodel represents the concepts, relationships, and semantics of a domain.

This paper is organized as follows. A brief description of different concept concerning software product line and variability is presented in the next section. Our previous work, which is the comparison framework, is described in section 3. An intentional software product line model is presented in section 4. In section 5 we present our proposed process. The section 6 concludes this work with our contribution and research perspectives.

II. SOFTWARE PRODUCT LINE AND VARIABILITY CONCEPTS

Software product lines are recognized as a successful approach to reuse in software development (Clements & Northrop, 2001; Bosch, 2000). The idea behind software product line is to economically exploit the commonalities between software products, but also to preserve the ability to vary the functionality between these products. These differences refer to the variability which is a key success factor in product lines and reuse. This approach is based on the undertaking of the development of a set of products as a single, coherent development activity. Indeed, products are built from a collection of artifacts from a core asset base that have been specifically designed for use. Core assets include not only the architecture and its documentation but also specifications, software components, tools...

Variability is the ability of a system to be efficiently extended, changed, customized or configured for use in a particular context (Van Grup, 2000). Another definition presents variability as the ability of a system, an asset, or a development environment to support the production of a set of artifacts that differ from each other in a preplanned fashion (Czarnecki & Eisenecker, 2000). In this definition variability means the ability of a core asset to adapt to usages in the different product contexts that are within the product line scope. Indeed,

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variations in a product line context must be anticipated.

The purpose of Variability modeling is to present an overview of a product line's commonality and variability. Variability modeling terms concerns also commonality modeling. The content of a variability model serves as a basis for defining variability within the artifacts that make up the product-line infrastructure as well as for configuring individual product instances and deriving them from the infrastructure.

SPL engineering is defined (Czarnecki & Eisenecker, 2000) by distinguishing two levels of engineering: Domain Engineering and Application Engineering as presented in Fig. 1.

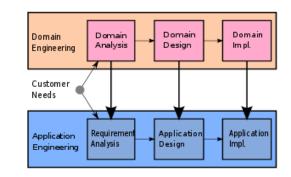


Fig. 1 : SPL Engineering levels

Domain Engineering corresponds to the study of the area of product line, identifying commonalities and variabilities among products, the establishment of a generic software architecture and the implementation of this architecture. Indeed, the domain engineering consists on the construction of reusable components known as asset which will be reused for the products building.

Application Engineering is used to find the optimal use for the development of a new product from a product line by reducing costs and development time and improve the quality. At this level, the results of the domain engineering are used for the derivation of a particular product. This derivation corresponds to the decision-making towards the variation points.

In the literature, the majority of variability research concerns requirements and architecture. But some works deals with implementation, verification and validation, traceability and software product line management. The literature basically proposes methods or techniques that address only a specific portion of SPL development.

III. COMPARISON FRAMEWORK

We have elaborated a framework to compare different approaches for the construction of SPL. The idea is to consider a central concept (SPL) on four different points of view. Defining a comparison framework has proved its effectiveness in improving the understanding of various engineering disciplines (process, requirements, information systems...) (Rolland, 1998; Jarke & Pohl, 1993). Therefore, it can be helpful for the better understanding of the field of engineering SPLs. As a result, our framework (Fig. 1) is presented in (Ouali et al., 2011).

The framework analysis allows us to identify the following main drawbacks of existing SPL construction methods. We realize that we have a lack of sufficient tool support for them and for their interactivity with their users. The SPL approaches themselves are not enough automated for deriving automatically a product from a SPL. In addition, these methods didn't cover all aspects of SPL engineering. Indeed, every method tries to focus on a particular part of SPL construction process. Finally, in these methods, apart requirement approaches ones, the problem is the matching between users' needs and the product offered by developers. Many writers have observed that there is a "conceptual mismatch" (Woodfield, 1997; Kaabi, 2007).

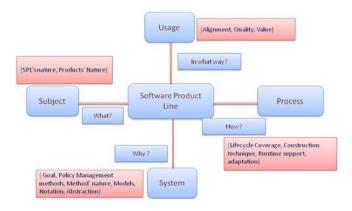


Fig.2 : Software Product Line comparison framework evolution

We try in the next section of this paper to resolve this last drawback by the proposal of a model for intentional SPL modeling. We try to establish the matching between users' needs and the product offered by developers by the expression of users' needs in an intentional way.

IV. INTENTIONAL SOFTWARE PRODUCT Line Meta-Model

This section describes a meta-model synthesizing the different interesting points that we previously identified after a state-of-the-art (software product line, intention, feature...). We chose to transform this meta-model into a UML profile to facilitate the integration into UML models and to use it in our MDA approach.

a) Meta-model Description

As depicted in Fig. 3, a *product line* contains *features*. A *product* belongs to one *product line* and is composed of *features*. These *features* associated to a product must check some constraints (mutual exclusion

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and require relation) throw the conflict and require relationships. The recommends relationship concerns another feature that could be pertinent.

An intentional software product line is a set of *features* captured at the business level, in business comprehensible terms and described in an intentional perspective. In this perspective, we focus on the *intention* it allows to achieve rather than on the functionality it performs. A *feature* is a set of related *requirements* that allows the user to satisfy an *intention*. We have two specializations of features which are *MandatoryFeature* and *VariantFeature*. Mandatory features are features which must be present in every configuration of a product from the product line.

A variant feature is modeled as a set of variation point. The metamodel allows atomic variation points *(Variant)* or composite ones *(Composite VariationPoint)* for a variant feature. We use the composite pattern to compose a variation point.

In our meta-model, we use a part of an existing meta-model map (Rolland et al., 1999c) which is a Process Model in which a non-deterministic ordering of intentions and strategies has been included. Map is a labeled directed graph with intentions as nodes and strategies as edges between intentions. A *map* consists of a number of sections. Each section is a triplet formed by a source *intention*, a target *intention* and a *strategy*. A *strategy* is a manner to achieve an *intention*.

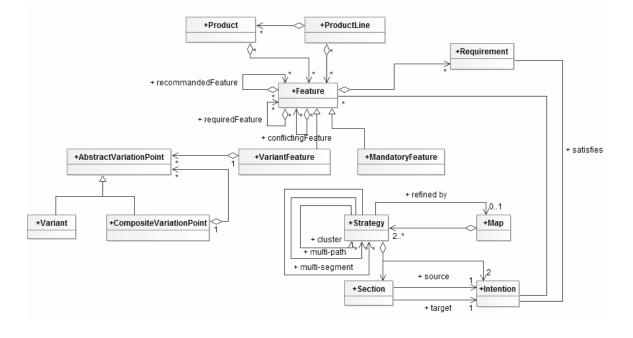


Fig.3: Above is the example of single column image. Images must be of very high quality.

V. PROPOSED PROCESS

To avoid the drawbacks of the existing methods, we try to propose a new process for the construction of SPL. This process is a flexible approach for automatically building SPL based on variability models. This process is based basically on goal modeling, features modeling, metamodels, constraints...

In our process, we try to cover domain engineering and application engineering. The domain engineering process involves the creation of core assets. In this process, our interest concerns the elicitation of intentions and strategies using the MAP for the design of users' requirements. A map is a process model expressed in a goal driven perspective which can provides a process representation system based on goals and strategies. The directed nature of the graph shows which goals can follow which one. MAP is considered as Intention-oriented process modeling which follows the human intention of achieving a goal as a force which drives the process (Soffer & Rolland, 2005). Having represented software product line features intentionality as maps, we will proceed in our process to determine features and their composition according to the Intentional Software Product line. This approach is presented in Fig. 4. Users' intentions are captured and modeled using Map Model to obtain an SPL Model. This model contains an intentional view. Variability in intentional software product line modelling is mandatory and due to the need to introduce flexibility in intention achievement. We use features diagrams to model variability in software product line. We try to capture commonality and variability of domain and to reuse it for the derivation of a specific requirement model in application Level.

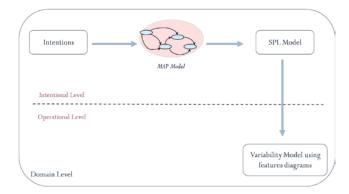


Fig.4 : Domain Level Engineering using Intentional Software Product Line Model

We try to manage variability in SPL construction process (functions, structures, behaviors, technologies). Our strategy follows feature modeling approach, MDA approach and the managing of the constraints. We base our work on the creation of features models representing the SPL structure. We use state machine to model the behavior in the SPL. This process is based on the automatic transformation of models until obtaining executable applications. The process is flexible because SPL developer has a lot of possibilities for the creation of SPL and its constraints. It permits the generation of a flexible SPL suitable to the users' requirements elicited in the beginning of the creation process and new ones.

VI. CONCLUSION

In this paper, our contribution was the proposal of a model combining software product line, variability, requirements and intentions. This suggested model clarifies the notion of an intentional software product line to model SPL in intentional context. It was build to respond to the following purpose: to focus on the intention it allows to achieve rather than on the functionality it performs. An intentional software product line is captured at the business level, in business comprehensible terms and described in an intentional perspective. This model will be useful to improve the method used for software product line construction by avoiding the conceptual mismatch. We try to establish the matching between users' needs and the product offered by developers by the expression of users' needs in an intentional way.

In this paper, we have presented a proposal to manage variability during the SPLs construction process using a MAP for goals modeling, features diagrams allows us to model the common and variable properties of product-line members throughout all stages of product-line engineering, metamodels allow the expression of common and variable characteristics of a set of applications.

Our future work will be the proposal of a tool support to improve interactivity with users and to cover the overall lifecycle of SPL. This tool support will be based on Eclipse plug-in for feature modeling using the Eclipse Modeling Framework (EMF), which significantly reduced our development effort. Our tool support is based on generative development for goal modeling, feature modeling and metamodels. Integrating goals modeling, feature modeling and metamodels as part of a development environment helps to optimally support modeling variability in different artifacts including implementation code, models, documentation, development process guidance...

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Towards full protection of web applications based on Aspect Oriented Programming

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Abstract - Web application security is a critical issue. Security concerns are often scattered through different parts of the system. Aspect oriented programming is a programming paradigm that provides explicit mechanisms to modularize these concerns. In this paper we present a technique for detecting and preventing common attacks in web applications like Cross Site Scripting (XSS) and SQL Injection using an aspect oriented approach by analyzing and validating user input strings. We use an aspect to capture input strings and compare them to predefined patterns. The intrusion detection aspect is implemented in AspectJ and is woven into the target system. The resulting system has the ability to detect malicious user input and prevent SQL Injection and Cross Site Scripting. We present an experimental evaluation by applying it to an insecure web application. The results of our tests show that our technique was able to detect all the attempted attacks without generating any false positives.

Keywords : symbolic information, artificial intelligence, Flow control, Architecture.

GJCST Classification: D.4.6, K.6.5,H.2.7



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Towards full protection of web applications based on Aspect Oriented Programming

Elinda Kajo Mece^{α}, Lorena Kodra^{Ω}

Abstract - Web application security is a critical issue. Security concerns are often scattered through different parts of the system. Aspect oriented programming is a programming paradigm that provides explicit mechanisms to modularize these concerns. In this paper we present a technique for detecting and preventing common attacks in web applications like Cross Site Scripting (XSS) and SQL Injection using an aspect oriented approach by analyzing and validating user input strings. We use an aspect to capture input strings and compare them to predefined patterns. The intrusion detection aspect is implemented in AspectJ and is woven into the target system. The resulting system has the ability to detect malicious user input and prevent SQL Injection and Cross Site Scripting. We present an experimental evaluation by applying it to an insecure web application. The results of our tests show that our technique was able to detect all the attempted attacks without generating any false positives.

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I. INTRODUCTION

ser and critically important company information is managed using web applications. For this reason, web applications serve as a door for attacks. The vulnerabilities present in the application can be exploited by an attacker. Even with the rapid development of Internet technologies, web applications have not achieved the desired security levels. As a result, web servers and web applications are popular attack targets.

Two common attacks on this type of systems are Cross Site Scripting (XSS) and SQL Injection. SQL Injection is a technique where an intruder injects SQL code into the user input field in order to modify the original structure of the query to post hidden data, or execute arbitrary queries in the database. Cross Site Scripting occurs when an intruder injects and executes scripts written in languages like JavaScript or VBScript.

Aspect Oriented Programming is a programming paradigm that provides explicit mechanisms to modularize crosscutting concerns (behavior that cuts across different divisions of the software) such as security. This makes it a good candidate for applying security to a system.

In this paper, we propose an Aspect Oriented protection system that detects and prevents attacks on web applications. This system analyzes and validates

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user input strings. We use an aspect to capture input strings and compare them to predefined patterns. The intrusion detection aspect is implemented in AspectJ and is woven into the target system. The resulting system has the ability to detect malicious user input and prevent SQL Injection and Cross Site Scripting. The advantage in using aspect oriented programming lies in separating the security code from application code. In this way it can be developed independently to adapt to new attacks.

The rest of the paper is organized as follows. Section 2 presents principles of SQL Injection, XSS and AOP. Section 3 presents related work in this area and our proposed solution. Section 4 describes in detail the architecture of our system and its integration with the web application. Section 5 describes the experimentation and evaluation results. Section 6 concludes and discusses some future work.

II. BACKGROUND

a) SQL Injection

SQL Injection consists in inserting malicious SQL commands into a parameter that a web application sends to a database in order to execute a malicious query. As a result, database contents can be corrupted or destroyed. The most popular techniques used in SQL injection are tautology, union, and comments.

The general idea behind tautology is finding a disjunction in the WHERE clause of a SELECT or UPDATE statement and inserting malicious code into one or more conditional statements so that they always evaluate as true. Let us consider the case where the web application authenticates users by executing the following query:

SELECT * FROM users WHERE username = 'admin' and password = 'pass' This query doesn't select any rows because the password is incorrect. Injecting ' OR 1=1 gives: SELECT * FROM users WHERE username = 'admin' and password = '' OR 1=1'

This causes the WHERE clause to be true for every row and all table rows are returned.

The UNION clause allows the chaining of two separate SQL queries. An attacker can use this clause to manipulate an SQL statement into returning rows from another table. As an example, consider the following query that allows users to get the product name by inserting the product ID.

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SELECT productName FROM products WHERE
productID = '5'

An attacker can use the UNION clause to modify the structure of this query to:

SELECT productName FROM products WHERE productID = '5' UNION SELECT username, password FROM users

As a result, this query will display the product name together with the usernames and passwords of the users table.

Another type of SQL Injection uses comments to cut an SQL query and change its structure. The part of the SQL statement that comes after the comments will not be executed and the query will return the results that the attacker wanted. For example the following SQL statement:

SELECT * FROM users WHERE username = 'alice' and password = 'alice123' can be transformed in the following way:

SELECT * FROM users WHERE username =
`admin' -- and password = `'

The query will return all the information about the admin user.

b) Cross Site Scripting

Cross Site Scripting (XSS) is an attack done towards the user's browser in order to attack the local machine, steal user information or to spoof the user identity. The attacker uses a web application to send malicious code usually in the form of a script. Together with the legitimate content, the users get the malicious script from the web application. This attack is successful in web applications that do not validate user input.

c) Aspect Oriented Programming and Security

Aspect Oriented Programming is а programming paradigm whose aim is to solve problems like code scattering and code tangling that cannot be solved by traditional programming methodologies. Code scattering means that the problem code is spread over multiple modules. This means that when developers want to fix a bug they have to modify several source files. Code tangling means that the problem code is mixed with other code. In the case of web applications, security code needs to be applied in different modules of the system. This process is error prone and difficult to deal with. AOP is a good candidate for applying security in web applications. The security code can be encapsulated into modules called aspects which can be maintained separately from the web application in order to adapt to new attacks.

III. RELATED WORK AND PROPOSED Solution

During recent years, different solutions have been proposed to address security issues in web applications. The most efficient way to protect against XSS and SQL Injection attacks is to inspect all the data the user inserts into the system, hence most of the work in this area treats user input.

Zhu and Zulkerine propose a model-based aspect-oriented framework for building intrusion-aware software systems [2]. They model attack scenarios and intrusion detection aspects using an aspect-oriented Unified Modeling Language (UML) profile. Based on the UML model, the intrusion detection aspects are implemented and woven into the target system. The resulting target system has the ability to detect the intrusions automatically.

Mitropoulos and Spinellis propose a method for preventing SQL Injection attacks by placing a database driver proxy between the application and its underlying relational database management system [1]. To detect an attack, the driver uses stripped-down SQL queries and stack traces to create SQL statement signatures that are later used to distinguish between injected and legitimate queries. The driver depends neither on the application nor on the RDBMS.

Hermosillo et al. present "AProSec" implemented in AspectJ and in the JBoss AOP framework, a security aspect for detecting SQL Injection and XSS [3]. They use the same aspect for dealing with SQL Injection and XSS. Their experiments show the advantage of runtime platforms such as JBoss AOP for changing security policies at runtime.

We propose a system that performs a two-step validation of user input. In the first step it is validated syntactically to check whether it contains dangerous characters that can be used in XSS and SQL Injection. In the second step, the input is validated by the SQL validator in the context of a query to check whether it contains always true statements, comments or combinations of SQL keywords. In contrast to the systems described above, our system analyzes directly user input before it is being used as part of an SQL query. This facilitates the analyzing process. Another advantage of our system is the fact that the SQL validator checks the presence of SQL keywords in the user input. This prevents attacks that do not contain comments or always true statements but contain SQL keywords that can modify the original structure of the SQL query. Our system does not generate false positives because it considers as attack the presence of a combination of SQL keywords and not the presence of a single SQL keyword such as "Union" that might be part of a legitimate user name.

IV. SYSTEM ARCHITECTURE

Our system consists of three parts. The first and the most important part is an aspect called WebAppInputFilter that contains the logic of the whole defense process. It defines the advices that control the validation process as well as the steps to be taken (code to be executed) based on the results of the

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validation. The aspect also contains the pointcuts that define the vulnerable points of the web application and allow the weaving with the advice code. The second part consists of a validators class that validate against XSS and SQL Injection attacks the input defined in the advices. The third part consists of an encoder which encodes dangerous characters by converting them to their decimal equivalent, leaving them harmless.

The basic idea behind our technique is to capture user input and validate it by comparing it to predefined patterns. In the case of SQL Injection, in contrast with current solutions [1, 2, 3], the user input is validated before being used as part of a query. The final query is a combination of user input and a partial SQL statement defined by the developer. We consider as safe the part of the query that is defined by the developer, so there is no need to validate it and we only validate the user input part. This facilitates and speeds up the evaluation process.

The validation process happens in two steps. First the user input is validated to check whether it contains dangerous characters such as '<',' >', '=' and' -' that can be used to perform XSS and SQL Injection attacks. In the second step, the SQL Validator analyzes the input in the context of the query. This is done to check whether the query contains combined SQL keywords that can modify the original structure of the query or SQL code that can transform the original query in an SQL statement that results always true.

Figure 1 shows the flow of information within the defense system. The aspect captures the user input string and sends it to the first analyzer. If the string is not dangerous it is passed on to the second validation step. If the string is dangerous it is send to the encoder. It encodes the dangerous characters and the result is passed to the SQL Validator. If the string is not considered dangerous, it is passed on to the web application as a legitimate request. If it is considered dangerous, it is erased.

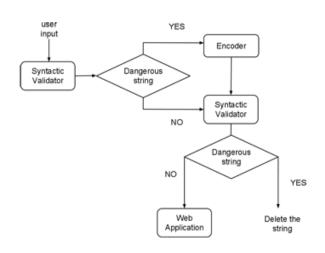


Fig. 1 : The flow of information within the defense system

a) The WebAppInputFilter Aspect

This aspect is implemented in AspectJ [7]. This is the most widely used language for aspect oriented programming. It represents the extension of Java for dealing with aspects. The aspect defines pointcuts in the vulnerable points of the web application. It monitors the traffic in servlets and captures some specific calls that implement the *ServletRequest* and *HttpServletRequest* interfaces. The pointcuts are:

pointcut pcGetParameter(): call(String javax.servlet.http.HttpServletRequest.ge tParameter (String))

pointcut

pcGetParameterValues():call(String []
javax.servlet.ServletRequest.getParamete
rValues(String))

b) The Validators

The validators class handles both XSS and SQL Validators. It uses regular expressions and pattern matching to validate user input against specific patterns.

The syntactic validator, analyzes separately each character of the user input string and acts as a filter that allows only characters 'a-z', 'A-Z', numbers '0-9', spaces and characters like "." and ",". The rest of the characters are considered dangerous and will be sent to the encoder.

The SQL Validator consists of several validation strings in the form of regular expressions that are matched against user input according to different possibilities of injecting SOL code into the user input field of the web application. The validation criteria include: always true comparisons (both string and numeric), presence of quotes or comments, keywords for executing stored procedures, combinations of SQL keywords like UNION, SELECT, DROP, INSERT, ALL, etc. As regards this least evaluation criterion, it protects in cases where no comments or always true statements are present in the query but it still may contain dangerous keywords that can execute arbitrary operations in the database. We would also like to emphasize that the SQL Validator doesn't simply detect the presence of SQL keywords, but the presence of combined SQL keywords that would potentially modify the original structure of the query. This means that input strings that simply contain SQL keywords (like UNION) will not be considered dangerous unless they contain some other SQL keyword that would create a risk for SQL Injection. This eliminates the false positive case of detection when a legitimate user has for example the word "Union" in their name.

V. EVALUATION RESULTS

We evaluated our system by using it against a vulnerable web application [8]. First we tried all sorts of SQL Injection and XSS injection attacks to see how the system behaved. Then we protected it using our system

but were unable to bypass the application's security.

For example, let's assume that an attacker tries to input the following script into the web application:

<script>alert(document.cookie)</script>

The system will detect the dangerous characters "<", ">", "(", ")" and "/" and encode them. In this way this input string will be considered as a simple string and not as a script and will not be interpreted by the browser. A wiser attack would be to encode the input string by using some encoding scheme (decimal, hexadecimal, octal, Unicode, etc) prior to inserting it into the web application. For example, the above string in hexadecimal format (\xNN) would be:

\x3c\x73\x63\x72\x69\x70\x74\x3e\x61\x6c \x65\x72\x74\x28\x64\x6f\x63\x75\x6d\x65 \x6e\x74\x2e\x63\x6f\x6f\x6b\x69\x65\x29 \x3c\x2f\x73\x63\x72\x69\x70\x74\x3e

Even in this case the attack wouldn't be successful because the system detects the usage of "\" and encodes the string to make it harmless. We tested our defense system by using other encodings (decimal, octal and Unicode) and none of the attacks were successful.

In the case of SQL Injection, let's assume that an attacker tries to inject a query that contains a statement that is always true into the system:

SELECT * FROM user_data WHERE last_name
= 'Smith' OR `1'='1'

The SQL Validator will detect that there is a statement that is always true and will delete this string without passing it to the web application.

In order to evaluate the impact of the defense system in the performance of the web application we measured its response time using [9] under two scenarios. We measured the response time first in the absence of any defense and then in the presence of our defense system. We used a mix of input strings: harmless, XSS attack and SQL Injection attack strings. For every scenario we used 356 POST and 104 GET requests which make a total of 460 requests. We executed the series of requests 5 times and measured the average response time. Our defense system introduced an average overhead of 2.11%. We feel that this is an acceptable level of overhead for use in many production environments and it will not be noticeable by the user.

VI. CONCLUSIONS AND FUTURE WORK

We have presented our approach for building a security system for a web application. This system detects XSS and SQL Injection attacks in requests. Our system was built separately and the initial code of the web application was not modified. This allows the

separation of security concerns and allows the security system to be evolved independently from the web application to adapt to new attacks.

As an advantage to similar solutions, besides checking for comments and always true statements, our SQL Validator also checks for the presence of a combination of SQL keywords in the input string. This can protect in cases where comments or always true statements are not present in the query but it still may contain dangerous keywords that can execute arbitrary operations in the database. Our system does not simply check for SQL keywords but for a combination of them. This is considered as an advantage in eliminating false positives like in the case of having for example the word "Union" as part of a legitimate user name. Furthermore, in contrast to usual solutions, when protecting against SOL Injection our system analyzes directly the user input before being used as part of a query. There is no need to analyze the whole query because the other parts of it are defined by the developer and are considered safe. This has the advantage of facilitating and speeding up the evaluation process.

Our system can be improved in some directions. A possible improvement might be the implementation of defense against other form of attacks. Also new techniques like *machine learning* and *neural networks* can be used to detect more sophisticated attacks. Another direction of improvement might be the implementation of *runtime weaving* using the JBoss AOP Framework [10].

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Digital Watermarking: Digital Data Hiding techniques for BMP Images

By Tripat Deep Singh Dua

Guru Nanak Institute Of Management And Technology Model Town Ludhiana , Punjab India

Abstract - Purpose: This research evaluates the digital watermarking technology further for hide/retrieved data into the BMP file by manipulating the contents their pixel value using least significant bits (LSB) approach. Methodology: Various experiments have been applied on the pixel value of the BMP file to hide/store the maximum data. With a condition the size and the quality of the BMP file will not change. The trail and error methods have been used or applied to check the various sizes with various qualities. Findings: The study finds that the any digital data can be hiding into the BMP file by manipulating the contents of the Red Green Blue (RGB) value by applying least significant approach. Originality/Value: Due to the growing usage of multimedia content on the internet, serious issues have emerged. Counterfeiting, forgery fraud and pirating of this content are rising. The research is a mechanism which can help resolve the ownership issues for digital data.

Keywords : Digital watermarking, Digital Data, 24 bit BMP Image, Red Green Blue (RGB) pixel Value, Least Significant Bit (LSB), lower order bits, Copy right, Tracking,

GJCST Classification: D.2.11



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Tripat Deep Singh Dua

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I. INTRODUCTION

Due to the growing usage of multimedia content on the internet, serious issues have emerged. Counterfeiting, forgery fraud and pirating of this content are rising. Virtually anyone with a sound card, scanner, video frame grabbers or multimedia authoring systems allow them to incorporate copyrighted material into presentations, web designs and internet marketing campaigns. A simple search on any of the search engines returns hundreds and thousands of images which can be easily downloaded on to a personal computer. The desire for the availability of information and quick distribution has been a major factor in the development of new technology in the last decade. There is the increased use of multimedia across the internet. Multimedia distribution has become an important way to deliver services to people around the world. It is commonly applied in internet marketing campaigns and electronic commerce web sites.

Digital Watermarking describes methods and technologies that hide information on bmp images, for example a number, text, image, video in any digital media. The embedding takes place by manipulating the content of the digital data, which means the information is not embedded in the frame around the data. For images this means that the modifications of the pixel values have to be invisible. In other words it is a pattern of bits inserted into a digital image, audio or video file that identifies the file's copyright information (author, rights, etc.). The name comes from the faintly visible watermarks imprinted on stationery that identify the manufacturer of the stationery. The purpose of digital watermarks is to provide copyright protection for intellectual property that's in digital format. Digital watermarks on the images are designed to be completely invisible, moreover, the actual bits representing the watermark must be scattered throughout the file in such a way that they cannot be identified and manipulated.

The left size picture is true 24 BMP 2.25 MB Image file and right size picture is 2.25 KB image Jpg picture that is to be inserted in left size image (True 24 bit bmp file)



BMP file 2.25 MB

Jpg file 2.25 KB

Author : Assistant Professor, Guru Nanal Institute of Mgt & Tech, Gujarkhan Campus, Model Town Ludhiana.

The picture shows half of the right picture has been inserted into the left picture that is original BMP file. When we insert the jpg 2.25 KB file into 2.25 MB file the by manipulating the pixels of the BMP file the size will remains same 2.25 MB and the image quality will also remains same.

Digital Watermarking describes methods and technologies that hide information on bmp images, for example a number, text, image, video in any digital media. The embedding takes place by manipulating the content of the digital data, which means the information is not embedded in the frame around the data. For images this means that the modifications of the pixel values have to be invisible. The research will be able to water mark the BMP images with bmp, jpeg, video, audio or any other format files. Digital Watermarking, which is used to transfer or pass information in a manner that the very existence of the message is unknown. With this study we can hide any type of file with any format into a 24 Bit True BMP with password protection (password can be encrypted using any of the existing encryption technology). For example we can hide or insert a video file or an audio file into a given BMP file without changing the image or its size. 24 Bit BMP format has been chosen because of its large pixel data. More the number of pixels in the image more data we can embed in it. This manipulation neither changes the image nor its size. i.e. the Image quality and its original size is maintained.

The first part of this study depicts the overview of the digital watermarking and defined the problem. Second part discusses the objectives of the study. The third part review the findings of the scholars who studied the watermarking in the past. Fourth chapter discusses the methodology used for the research purpose. The fifth part shows the analysing of the data and experiments. The sixth part shows the findings and colclusion.

II. OBJECTIVE OF THE STUDY

The study aims the following objectives

The objective of our study would be to develop a technique which would embed some kind of information into the digital multimedia content such that the information (which we call the watermark) can later be extracted or detected for a variety of purposes including copy prevention and control and will help us to address some of the challenges faced by the rapid proliferation of digital content.

- Watermark should remain invisible
- Use of any digital content as watermark
- Copy/Copyright protection

III. REVIEW OF LITERATURE

There is no dearth of literature on watermarking. A number of scholars investigated the diffrents aspects of the topic. *Zhao and Koch (1995), Cox et al (1997), Hartung, Eisert, and Girod (1998), Hsien Fu (1998), Hsu and Wu (1998, 1999), Zhao et al. (1998), Unzign and Stirmark (1999), Fei et al (2001), Hasslacher (2004), Saryazdi & Hossein (2005) and Agrawal (2007)* evaluated the topic digital watermarking.

An interface has been defined in the watermark agent to the external watermark retrieval library.

This interface employs Java's native interface technology to allow Java objects to call watermark retrieval functions written in C. At present, SysCoP (*Zhao and Koch 1995*) is the only digital watermarking mechanism that has been supported in the watermark agent. However, the watermark agent can easily support any other watermarking system.

Cox et al (1997) describe a method for embedding a binary watermark sequence in the highest magnitude DCT coefficients.

Hsien Fu (1998) conducts a literature survey of digital watermarks used for images. It describes the previous work done on digital watermarks, including the analysis of various watermarking schemes and their results. Potential applications are discussed, and an implementation plan of the project is presented. Hsien Fu uncovers the fact that recent work has shown that digital watermarks can be fairly successful in achieving the desired properties mentioned in section 2. These watermarks, however, are not perfect, and more could be done to improve a watermark's robustness or accuracy in detection. Furthermore, the question of copyright infringement remains a legal issue. Courts need to determine which methods may or may not be used. Until these legal standards are set, the Internet continues to be unsafe for images.

Hartung, Eisert, and Girod (1998) studied the methods for digital watermarking of MPEG-4 facial animation parameter data sets. They used a model-based approach for the estimation of the facial parameters that combines a motion model of an explicit 3D textured wireframe with the optical flow constraint from the video data. This leads to a linear algorithm that is robustly solved in a hierarchical framework with low computational complexity. Experimental results confirm the applicability of the presented watermarking technique.

Hsu and Wu (1998, 1999) use the middle frequency coefficients of DCT/Wavelet transform to embed a binary watermark. These mentioned methods are robust against image processing. Their main drawback is requiring the original image to extract the watermark.

Digital watermark has found a multitude of potential applications other than the originally motivation for copyright protection *(Zhao et al. 1998).* Similarly, the various types of uses of the watermark agents create many spectrums and great business opportunities.

Zhao and Luo (1998) presents a complete

digital watermark agent system to effectively put the digital watermark technology into practice. This system enables an agency to dispatch digital watermark agents to agent servers and agent can perform various tasks on the server. Once all the actions have been taken, a report will be sent to an agency's database and an agent can continue to travel to another agent server.

Unzign and Stirmark (1999), integrate a variety of geometric attacks. Unzign introduces local pixel jittering and is very efficient in attacking spatial domain watermarking schemes. Stirmark introduces both global and local geometric distortions. We give a few more details about these attacks later in this paper. However, most recent watermarking methods survive these attacks due to the use of special synchronization techniques. Robustness to global geometric distortions often relies on the use of either a transform in variant domain (Fourier-Melline) or an additional template or of specially designed periodic watermarks whose auto covariance function (ACF) allows estimation of the geometric distortions.

Fei et al (2001) attempt to find a suitable transform domain to watermark images robust against JPEG compression attack. They show that the choice of the transform domain depends on the type of the embedded information. If the watermark is embedded by repetition coding, then the Hadamard transform gives the best results.

Hasslacher (2004) evaluates the watermarking and its uses. He reveals that the scaling factor is a critical system parameter. If ais too small. The image is not distorted but the robustness of the scheme is low. He also unearth that Modification of low-frequency coefficients distorts the image and Gives the hacker a clue about where the watermark is embedded.

Wang et al. (2004) describe a kind of blind watermarking based on relative modulation of the DCT coefficient value by referring to its estimated one. In their method, the DC values of a 3×3 neighborhood of 8×8 blocks are used to estimate the AC coefficients of central block. In each group of nine 8×8 blocks, five bits of watermark are embedded by modulating the first five DCT AC coefficients, in central block, with the following rule:

Set ACi \square AC'i \square \square to embed bit "1"

Set ACi \square AC'i \square \square to embed bit "0"

Where, ACi and AC'i are the real and estimated value of the AC coefficients, respectively. The watermark recovery is done by comparing ACi and its estimated value. If ACi \Box AC'i, then the extracted bit is "1", otherwise, it is "0".

Saryazdi & Hossein (2005) propose a blind scheme for gray-level data embedding in Hadamard Domain. In the proposed algorithm, the host image is first divided into 4×4 non-overlapping blocks. Their embedding procedure contains two parts. The first part

is estimating the first two Hadamard low frequency AC coefficients (i.e. H(0,2) and H(2,0)) in each block, using its neighbor blocks. We use the following equations, to estimate the low frequency AC Hadamard coefficients of a block using the DC values of its 3×3 neighbor blocks. Saryazdi & Hossein (2005) concludes that For most watermark application, it is desired to recover the embedded data without using host image. In this paper, such a watermarking scheme for embedding gray-level watermarks is presented. In the proposed method, the two first Hadamard AC coefficients are estimated by their neighbor blocks. Then, a number proportional to the gray-level watermark value is added to each estimated AC coefficient. The recovery procedure consists of comparing the estimated values with actual ones.

Agrawal (2007) propose a robust perceptual digital video watermarking procedure to embed a watermark image in digital video frames using the variable-temporal length 3-D DCT technique. He finds that in many existing video watermarking schemes, the raw video is needed for detection of watermark logo. This is refered to as non-blind method and is not convenient in many cases. In this thesis we propose a new blind watermark detection algorithm. The performance of the blind detection technique was evaluated for several types of video sequences. The watermarking is also done for color video samples in the YUV domain. We used only the luminance (Y-Component) to embed the watermark to make the watermarking scheme more robust since the chrominance (U and V) components is perceptually less sensitive to human visual system compared to the luminance (Y-Component).

Research scholars evaluated the different aspects of the digital watermarking and revealed a number of facts about the technology but not much research has been done on the watermarking on BMP files and on the method to hiding an BMP image in other without changing its view. This research will concentrate on the said topic.

IV. RESEARCH METHODOLOGY

Digital watermarking techniques can be used successfully with digital content in various forms like still images of bmp format using their least significant bit (LSB). In LSB substitution the lower order bits of selected pixels in the image are used to store watermarks. Techniques like flipping the lower order bits, replacing the lower order bits of each pixel with higher order bits of a different image (for e.g., a company logo), superimposing a watermark image over an area of image to be watermarked and adding some fixed intensity value are used to embed watermarks in spatial domain.

In Least Significant Bits substitution the lower order bits of selected pixels in the image are used to

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store watermarks or the LSB's are replaced with the higher order bits of the data that is to be inserted in the image that will effect the slightest change in the colour value of the pixel but non noticeable in colour point of view. The 24 bit BMP data has been chosen because of large pixel value more the file size the more data can de hide into the file. Any logo, signature, company name, bmp image or nay image or any audio/ video file can be hide into BMP file. More over the size of the original image will remain same after embedding the other data into the image. Because the data that is to be hide into the BMP file that is not any extra data or not any embedded data but the original data that will be replaced with the original lower bits of the BMP file. The idea behind this technique is that modifying the LSB will not make much difference to the color of the pixel.

a) Methods used for the study

Experimentations refer to the used of the new techniques to innovate/implement new idea for experiment based. The research refers to the experiments to hide any digital data into the BMP file

format so that it can be easily inserted into the 24 BMP file. The hidden data will be into the BMP file with its original form and will not loose its originality. On the other side the size of the BMP file will remains same because it will not increase in any case because the BMP has given the full space to the data that is to hide in the BMP file by replacing its contents or called pixels. The experiment is successful because the BMP file can store large data because of its large file size. And experiment is again successful when the quality/colour of the BMP file will remains same to normal human eye after modifying the contents of the pixel value in the BMP file.

First convert the file to be **hidden into a binary stream** and then read the BMP file pixel by pixel and substituting the LSB's of R, G, B component of each pixel with the bits from the binary stream until the entire binary stream had been substituted into the image. The binary stream that is substituted also has a format for easy and fast retrieval. We use a 12 Byte or 96 Bit headers, which is prefixed, to the Binary Stream before being substituted.

V. EXPERIMENTS & ANALYSIS

Figure 1 : shows the actual 24 bit BMP image data

24 bit Image Data									
O									
Pixel 1,1	Pixel 1,2	Pixel 1,3	Pixel 1,4	•••••	•••••	•••••	•••••		Pixel 1,width
Pixel 2,1	Pixel 2,2	Pixel 2,3	Pixel 2,4						Pixel 2,width
Pixel 3,1	Pixel 3,2	Pixel 3,3	Pixel 3,4						Pixel 3,width
Pixel 4,1	Pixel 4,2	Pixel 4,3	Pixel 4,4						Pixel 4,width
Pixel _{Height} ,1	Pixel _{Height} ,2	Pixel ^{Height} ,3	Pixel _{Height} ,4						Pixel Height,w

The figure 1 shows how the pixels are stored in the form of BMP file. The data is stored in the form of the matrix of height and width. The RGB pixels are stored in the BMP file which describes the overall description of the image. The pixel value starts from (1,1) to until the size and the width of the picture.

	ری به در است و مشار می این	lmage PixelArr				
Pixel[0,h-1]	Pixel[1,h-1]	Pixel[2,h-1]		Pixel[vv-1,h-1]	Padding	
Pixel[0,h-2]	Pixel[1,h-2]	Pixel[2,h-2]		Pixel[w-1,h-2]	Padding	
Pixel[0,9]	Pixel[1,9]	Pixel[2,9]		Pixel[w-1,9]	Padding	
Pixel[0,8]	Pixel[1,8]	Pixel[2,8]		Pixel[w-1,8]	Padding	
Pixel[0,7]	Pixel[1,7]	Pixel[2,7]		Pixel[w-1,7]	Padding	
Pixel[0,6]	Pixel[1,6]	Pixel[2,6]		Pixel[w-1,6]	Padding	
Pixel[0,5]	Pixel[1,5]	Pixel[2,5]		Pixel(w-1,5)	Padding	
Pixel[0,4]	Pixel[1,4]	Pixel[2,4]		Pixel[w-1,4]	Padding	
Pixel[0,3]	Pixel[1,3]	Pixel[2,3]		Pixel(w-1,3]	Padding	
Pixel[0,2]	Pixel[1,2]	Pixel[2,2]		Pixel[w-1,2]	Padding	
Pixel[0,1]	Pixel[1,1]	Pixel[2,1]		Pixel[vv-1,1]	Padding	
Pixel[0,0]	Pixel[1,0]	Pixel[2,0]		Pixel[w-1,0]	Padding	

<i>Figure 2 :</i> of BMP Image Data in the form of array
$F[Qu] \in \mathbb{Z}$, of divin induce data in the joint of analy

Figure 2 shows the 24 bit BMP Image pixels are stored in the form of Pixel Array or Matrix. The height and the width of the pixels are adjusted according to the size of the image. The lowermost left pixel of the image describes the starting point or the starting pixel values of the height and the width of the pixel. For example in the image 2 it is clearly shown that the lower most value of in the pixel array is pixel (0, 0). As the size of the picture grows the values in the array grows according to the size of the image.



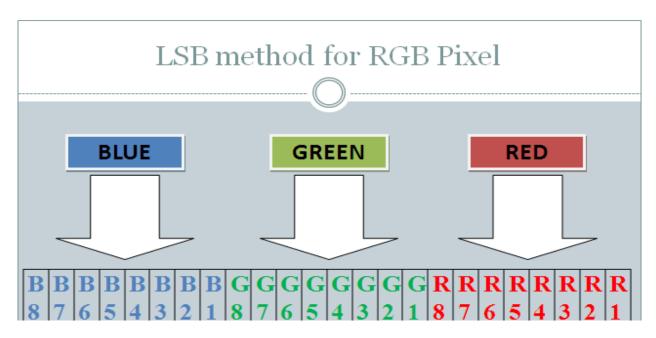


Figure 3 shows **pixel** Method of a 24 bit BMP file, the data is stored in the form 3 pixel RGB. The format of BMP file id 24 bit and each pixel has been

assigned a colour value 8 for RGB. This is the colour combination of each pixel where all the colours in the colour palette designed by the combination of RGB.

Figure 4





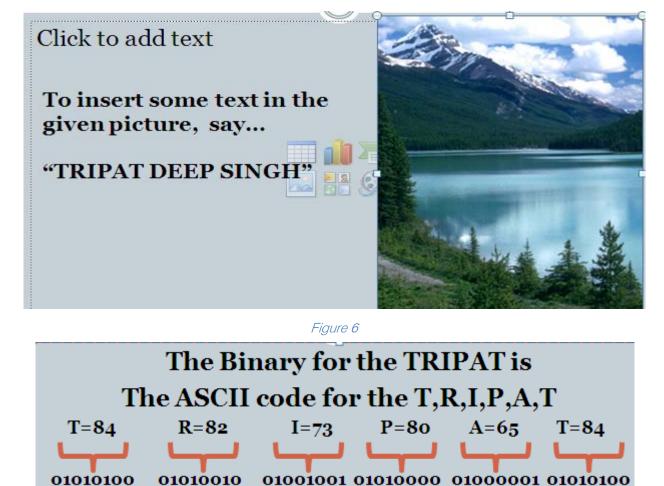
Picture to hide also bmp size 225 kb

The figure 4 shows that the left picture is true BMP format picture with a size of 2.25 MB and the right

side picture Jpg picture with a size of 225 KB that is to

be inserted in BMP file. The insertion takes place with the BMP file.

Figure 5 : shows to hide some text and a ring tone of a song into 2.25 MB file.



To hide something into BMP file, first covert the Binary for the TRIPAT into ASCII CODE, Than 24 bit RGB

pixel value for the sample colour for the starting image is

VI. FINDINGS AND CONCLUSION

Digital watermarks for BMP images on the images are designed to be completely invisible, moreover, the actual bits representing the watermark must be scattered throughout the file in such a way that they cannot be identified and manipulated.

The development will be able to water mark the BMP images with bmp, jpeg, video, audio or any other format files. The research is based on a Technique known as Digital Watermarking, which is used to transfer or pass information in a manner that the very existence of the message is unknown. With this study we can hide any type of file with any format into a 24 Bit True BMP with password protection (password can be encrypted using any of the existing encryption technology). For example we can hide or insert a video file or an audio file into a given BMP file without changing the image or its size. 24 Bit BMP format has been chosen because of its large pixel data. More the number of pixels in the image more data we can embed in it. This manipulation neither changes the image nor its size. i.e. the Image quality and its original size is maintained. Our study is based on a Digital Watermarking Technique known as Least Significant Bit (LSB's)

In this technique the LSB's of the Pixels are modified to store the information. The idea behind this technique is that modifying the LSB will not make much difference to the colour of the pixel. Multimedia distribution has become an important way to deliver services to people around the world. It is commonly applied in internet marketing campaigns and electronic commerce web sites. Due to the growing usage of multimedia content on the internet, serious issues have emerged. Counterfeiting, forgery fraud and pirating of this content are rising. Virtually anyone with a sound card, scanner, video frame grabbers or multimedia allow them to incorporate authoring svstems copyrighted material into presentations, web designs and internet marketing campaigns. A simple search on any of the search engines returns hundreds and thousands of images which can be easily downloaded on to a personal computer.

An important point that arises in these applications is the protection of ownership rights. Anybody and everybody can download digital content from the internet and can reuse or redistribute that as his own thus depriving the rightful owner of royalty or recognition for his/her work. Hence the need for developing new copy deterrence and protection mechanisms for digital content is felt.

We need to have a mechanism which can help resolve the ownership issues for digital content. The owner should be able to mark his work in some way which should later help in resolving the ownership in case of dispute. Moreover the mark should not affect the quality or the meaning of the image or should not change it. This process on hard copy of images is known as watermarking and when applied to digital content is known as digital watermarking. Consequently, copyright abuse is rampant among multimedia users who are rarely caught. This copyright abuse is the motivating factor for this study.

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Hotspot Identification System for identification of core residues in Diabetic Proteins

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Abstract - Data on genome structural and functional features for various organisms are being accumulated and analyzed in laboratories all over the world. The data are stored and analyzed on a large variety of expert systems. The public access to most of these data offers to scientists around the world an unprecedented chance to data mine and explores in depth this extraordinary information repository, trying to convert data into knowledge. The DNA and RNA molecules are symbolic sequences of amino acids in the corresponding proteins has definite advantages in what concerns storage, search, and retrieval of genomic information. In this study an attempt is made to develop an algorithm for aligning multiple DNA / protein sequences. In this process hotspots are located in a protein sequence using the multiple sequence alignment.

Keywords : Symbolic sequences, DNA, RNA, Protein sequence, Multiple Sequence alignment.

GJCST Classification: Optional, DDC/LCC/UDC/Global Journals/NLMC/FOR/MSC Classifications Accepted



Strictly as per the compliance and regulations of:



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Hotspot Identification System for identification of core residues in Diabetic Proteins

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Abstract - Data on genome structural and functional features for various organisms are being accumulated and analyzed in laboratories all over the world. The data are stored and analyzed on a large variety of expert systems. The public access to most of these data offers to scientists around the world an unprecedented chance to data mine and explores in depth this extraordinary information repository, trying to convert data into knowledge. The DNA and RNA molecules are symbolic sequences of amino acids in the corresponding proteins has definite advantages in what concerns storage, search, and retrieval of genomic information. In this study an attempt is made to develop an algorithm for aligning multiple DNA / protein sequences. In this process hotspots are located in a protein sequence using the multiple sequence alignment Keywords : Symbolic sequences, DNA, RNA, Protein sequence, Multiple Sequence alignment.

I. INTRODUCTION

n Bioinformatics, sequence alignment is a prominent method of arranging the sequences of DNA, RNA or protein to identify regions of similarity. Similarity may be functional, structural or evolutionary relationships between the sequences. Aligned sequences of nucleotide, amino acid residues are represented in a row form of a matrix. Identical or similar characters are aligned in successive columns by inserting gaps between the residues. There is a storm of revolution in the areas of Genomics and Bioinformatics in recent years. Bioinformatics is widely used for computational usage and processing of molecular and genetic data. The biologists considered Bioinformatics for the use of computational methods and tools to handle large amounts of data and make the data more understandable and useful. On the other hand, others view Bioinformatics as an area of developing algorithms and tools and to use mathematical and computational approaches to address theoretical and experimental questions in biology. As genomic data is rapidly exposed to increasing research, knowledge based expert system is becoming indispensable for the emerging studies in Bioinformatics. Hence validation and analysis of mass experimental and predicted data to identify relevant biological patterns and to extract the hidden knowledge are becoming important.

AAB24882	TYHMCQFHCRYVNNHSGEKLYECNERSKAFSCPSHLQCHKRRQIGEKTHEHNQCGKAFPT 60
AAB24881	IECNQCGKAFAQHSSLKCHYRTHIGEKPYECNQCGKAFSK 40
	**** *** * * ** * ****
AAB24882	PSHLQYHERTHTGEKPYECHQCGQAFKKCSLLQRHKRTHTGEKPYE-CNQCGKAFAQ- 116
AAB24881	HSHLQCHKRTHTGEKPYECNQCGKAFSQHGLLQRHKRTHTGEKPYMNVINMVKPLHNS 98
	**** * *****************

In recent years, semantic web based methods are introduced and are designed in such a way that meaning is added to the raw data by using formal descriptions of concepts, terms and relationships encoded within the data. To analyze and understand the data, today's information rich environment developed and designed a number of software tools. These tools provide powerful computational platforms for performing Insilco experiments (8). As there is much complexity and diversity in the analysis of tools, the need is for an intelligent computer system for automated processing. Present researches in Bioinformatics need the use of

Author ^B : Vice Chancellor, JNTUK, Kakinada

integrated expert systems to extract more efficient knowledge. In the biological process proteins undergo some interactions. These protein-protein interactions are mediated molecular mechanisms. During this interaction, a small set of residues play a critical role. These residues are called hot spots. The ability to identify the hot spots from sequence accurately and efficiently as expert system that enables and analysis of protein-protein interaction hot spots. This analysis may benefit function prediction and drug development. At present there is a strong need for methods to obtain an accurate description of protein interfaces. Many scientists try to extract protein interaction information from protein data bank.

Alignment Methods Used: In general the hot spots are identified as active sites in protein structures as binding is done using structures. The researcher tried 2012

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to find the hotspots in protein sequence rather than structure. In this process, taking into consideration the evolutionary history, the families of sequences are aligned using multiple sequence alignment.

In the process of alignment two methods are used Standard method using dynamic programming and A proposed alternative- MSAPSO (Multiple Sequence alignment using Particle Swarm Optimization) method in which alignment is performed using PSO technique. A comparison of these two methods also made. If the sequences are very short or similar they can be aligned by hand. But lengthy and highly variable numerous sequences cannot be aligned manually. To produce high quality sequence alignments, construction of algorithms and application of human knowledge are necessary. Computational approaches to sequence alignments are of two types- Global alignments and local alignments. Global alignment is the alignment to span the entire length of sequences whereas local alignments identify regions of similarity within the long sequences.

1. Particle Swarm Optimization: Particle Swarm Optimization (PSO) is based on stochastic optimization technique. It is one of the machine learning algorithms. It has been considered to be an effective optimization tool in many areas. The interesting point in PSO is that each particle with potential solution searches through the problem by updating itself with its own memory and also the social information gathers from other particles. Multiple Sequence Alignment: When three or more biological sequences namely protein, DNA or RNA are generally aligned, it is called multiple sequence alignment. As it is difficult and also time consuming to align by hand, computational algorithms are used to analyze and produce such biological sequences. Most multiple sequence alignment programs use heuristic methods as the

order of the sequences to align plays a vital role. Development of MSA algorithm is now an active are of research. MSA alignments are an essential tool for protein structure and function prediction, phylogeny inference and other common tasks in sequence analysis.

2. Pair wise Sequence Alignment: If two sequences are arranged for an alignment it is known as pair wise sequence alignment. The degree of relationship between the sequences is predicted computationally or statistically based on weights assigned to the elements aligned between sequences. The standard algorithm to align a pair of sequences is Needleman Wunch algorithm. This algorithm uses dynamic programming. In this study an algorithm PSAPSO (Pair wise Sequence alignment using Particle Swarm Optimization) is proposed and is also compared with the standard algorithm to know the accuracy of the results. A gene encoded in the genetic code defines the amino acid sequence in a protein. An amino acid residue is the combination of three nucleotides. Each three-nucleotide set is a codon. The set of codons forms a genetic code. For example AUG stands for methonine M. In this AUG is a codon. M is an amino acid and the residues A, U, G are nucleotides. Genes encoded in DNA are first transcribed into pre-messenger RNA (mRNA) known as primary transcript. Then pre-mRNA process to mature mRNA using various forms of modifications of posttranscriptional modifications. Then mature mRNA is used as a template for protein synthesis, which is known as translation onto a ribosome. Then read three nucleotides at a time by matching each codon to its base pairing anticodon to form transfer RNA (tRNA). Then tRNA recognizes the amino acid corresponding to the codon. The sequence thus obtained is protein sequence.



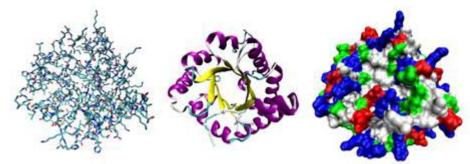
The amino acids in a protein sequence are shown in the following table.

One Letter	Three Letter	Full Name	One Letter	Three Letter	Full Name
G	GLY	Glycine	W	TRP	Tryptopham
А	ALA	Alanine	Y	TYR	Threonine
V	VAL	Valine	Ν	ASN	Asparagine
L	LEU	Leucine	Q	GLN	Glutamine
Ι	ILE	Lsoleucnie	D	ASP	Asparatic Acid
F	PHE	Phenylalanine	Е	GLU	Glutamic Acid
Р	PRO	Proline	K	LYS	Lysine
S	SER	Serine	R	ARG	Arginine
Т	THR	Threonine	Н	HIS	Histidine
С	CYS	Cyctenie	М	MET	Methinine

Table 1

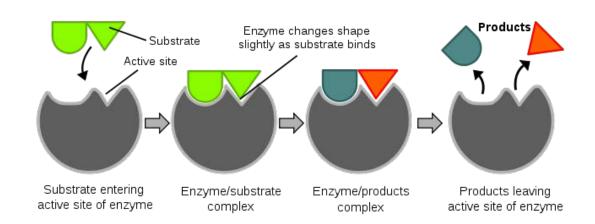
The overall structure and function of a protein is determined by the amino sequence. Most proteins fold into 3-dimensional structures and its shape is known as its native state. There are four levels in a protein structure.

- Primary Structure: Primary structure is nothing but an amino acid sequence. Secondary Structure: Secondary structures are regularly repeating local structures and are stabilized by hydrogen bonds. As they are local in nature different secondary structures can be present in the same protein molecule. Example alpha helix, beta sheet and turns.
- Tertiary Structure: Tertiary structure is the special relationship of the secondary structures to one another and is generally stabilized by the formation of the hydrophobic core, a non-local interaction. Salt bridges, hydrogen bonds; disulphide bonds and even post-transnational modifications also stabilize it. It mainly controls the basic function of the protein.
- Quaternary Structure: This structure is formed by several protein molecules i.e. poly peptide chains and it functions as a single protein complex.



- Enzymes: Enzyme is one of the functions of the protein which carries out most of the reactions involved in metabolic activities. Enzymes are proteins that increase the rate of chemical reaction. Adding or participation of the substance called catalyst does the change in the rate of chemical reaction. Catalysts that speed the reaction are called positive catalysts. Substances that interact with catalysts to slow the reaction are called inhibitors (or negative catalysts). Substances that increase the activity of catalysts are called promoters, and substances that deactivate catalysts are called catalysts are ca
- Active Sites in Proteins: An Active site is a part of an enzyme where substrates bind and

undergo a chemical reaction. The substrate which is a molecule binds with the enzyme active site and then an enzymesubstrate complex is formed. It is then transformed into one or more products, which are released from the active site. The active site is now free to accept another substrate molecule. In the case of more than one substrate, these may bind in a particular order to the active site, before reacting together to produce products. A product is something "manufactured" by an enzyme from substrate. For example the products of its Lactase are Galactose and Glucose, which are produced from the substrate Lactose.



Two models- the lock and key model and induced fit model are the two models proposed to describe how the enzymes work. In the lock and key model the active site perfectly fits for a specific substrate. If once the substrate binds to the enzyme no further modification is necessary. On the other hand in the induced fit model, an active site is more flexible and the presence of certain residues (amino acids) of the active site the enzyme is encouraged to locate the correct substrate. Once the substrate is gone conformational changes may occur. Hot spots are a set of residues recognized or bound in the process of interacting with other proteins. These are the residues in the active site.

II. RESULTS & DISCUSSION

Insulin is one of the important protein sequences which cause diabetes. So we tried to identify the hotspots in this protein sequence using the following methodology.

• The protein structures are retrieved from protein data bank by mapping with insulin protein sequence accession p01038 shown in the following table.

SNO	PDB Code	Chain	First PDB residue	Last PDB residue	First P01308 (INS_Human) residue	Last P01308 (INS_Human) residue
1	1a7f	А	1	21	90	110
2	1a7f	В	1	29	25	53
3	1ai0	А	1	21	90	110
4	1ai0	В	1	30	25	53
5	1ai0	С	1	21	90	110
6	1ai0	D	1	30	25	54
7	1ai0	Е	1	21	90	110
8	1ai0	F	1	30	25	54
9	1ai0	G	1	21	90	110
10	1ai0	Н	1	30	25	54
11	1ai0	Ι	1	21	90	110
12	1ai0	J	1	30	25	54

12	1ai0	J	1	30	25	54
13	1ai0	K	1	21	90	110
14	1ai0	L	1	30	25	54
15	1aiy	А	1	21	90	110
16	1aiy	В	1	30	25	53
17	1aiy	C	1	21	90	110
18	1aiy	D	1	30	25	54
19	1aiy	Е	1	21	90	110
20	1aiy	F	1	30	25	54
21	1aiy	G	1	21	90	110
22	1aiy	Н	1	30	25	54
23	1aiy	Ι	1	21	90	110
24	1aiy	J	1	30	25	54
25	1aiy	K	1	21	90	110

• Then identify the protein-protein interactions for each of these protein structures shown in the following table.

SNO	PDB Code	Chain	Chain
1	1a7f	А	В
2	1ai0	А	В
3	1ai0	В	D
4	1ai0	С	D
5	1ai0	Е	F
6	1ai0	F	Н
7	1ai0	G	Н
8	1ai0	Ι	J
9	1ai0	J	L
10	1ai0	K	L
11	1aiy	А	В
12	1aiy	В	D

13	1aiy	С	D
14	1aiy	Е	F
15	1aiy	F	Н
16	1aiy	G	Н
17	1aiy	Ι	J
18	1aiy	J	L
19	1aiy	K	L
20	1b9e	А	В
21	1b9e	В	D
22	1b9e	С	D
23	1guj	А	В
24	1guj	В	D
25	1guj	С	D

Identification of Hotspot: The hot spots are identified using these interfaces and the hot spots in the protein sequence p01308 are

MALWMRLLPLLALLALWGPDPAAAFVNQHLCGSHLVEALYLVCGERGFFYTPKTR

REAEDLQVGQVELGGGPGAGSLQPLALEGSLQKRGIVEQCCTSICSLYQLENYCN

III. CONCLUSION

Hot spots are of residues comprising only a small fraction of interfaces of the binding energy. We present a new and efficient method to determine computational hot spots based on pair wiser technique using potentials and solvent accessibility of interface residues. The conservation does not have significant effect in hot spot prediction as a single feature. Residue occlusions from solvent and pair wise potentials are found to be the main discriminative features in hot spot prediction. The predicted hotspots are observed to match with the experimental hot spots with an accuracy of 70%. The solvent is a necessary factor to define a hot spot, but not sufficient itself. This is also compared our methods and other hot spot prediction methods. Our method outperforms them with its high performance expert system.

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A Survey on Software Protection Techniques against Various Attacks

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Abstract - Software security and protection plays an important role in software engineering. Considerable attempts have been made to enhance the security of the computer systems because of various available software piracy and virus attacks. Preventing attacks of software will have a huge influence on economic development. Thus, it is very vital to develop approaches that protect software from threats. There are various threats such as piracy, reverse engineering, tampering etc., exploits critical and poorly protected software. Thus, thorough threat analysis and new software protection schemes, needed to protect software from analysis and tampering attacks becomes very necessary. Various techniques are available in the literature for software protection. The functionalities and the characteristic features are various software protection techniques have been analyzed in this paper. The main goal of this paper is to analyze the existing software protection techniques and develop an efficient approach which would overcome the drawbacks of the existing techniques.

Keywords : Software Security, Software Tampering, Tampering Attacks, Encryption, Cryptography, Decryption.

GJCST Classification: K.6.5



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A Survey on Software Protection Techniques against Various Attacks

N.Sasirekha^{α}, Dr.M.Hemalatha^{Ω}

Abstract - Software security and protection plays an important role in software engineering. Considerable attempts have been made to enhance the security of the computer systems because of various available software piracy and virus attacks. Preventing attacks of software will have a huge influence on economic development. Thus, it is very vital to develop approaches that protect software from threats. There are various threats such as piracy, reverse engineering, tampering etc., exploits critical and poorly protected software. Thus, thorough threat analysis and new software protection schemes, needed to protect software from analysis and tampering attacks becomes very necessary. Various techniques are available in the literature for software protection from various attacks. This paper analyses the various techniques available in the literature for software protection. The functionalities and the characteristic features are various software protection techniques have been analyzed in this paper. The main goal of this paper is to analyze the existing software protection techniques and develop an efficient approach which would overcome the drawbacks of the existing techniques.

Keywords : Software Security, Software Tampering, Tampering Attacks, Encryption, Cryptography, Decryption.

I. INTRODUCTION

Software protection has become one of the attractive domains with high commercial interest, from major software vendors to content providers which also comprises of the movie and music recording industries. The digital data of the software is especially at tremendous risk.

Confidentiality and data authenticity are two important concepts in security. Confidentiality provides data secrecy of a message and data authenticity protects the integrity of the message. Software protection falls between the domains of security, cryptography [30] and engineering among other disciplines.

The software protection technique mainly concentrates on protecting software from various attacks such as reverse engineering by obfuscation, modification by software tamper resistance, program-

Author ^Q: Head, Department of Software Systems, Karpagam University, Coimbatore, Tamilnadu, India. E-mail : hema.bioinf@gmail.com based attacks by software diversity, and BORE – breakonce run everywhere – attacks by architectural design [2].

Protecting content needs protecting the software which processes the content. Copy protection is another form of software protection to the level that it needs several same protections against reverse engineering and software tampering.

Protecting code from attacks such as reverse engineering [32], analysis and tampering attacks is one of the main concerns for software providers. If a competitor succeeds in obtaining and reusing a algorithm, it would result in major issue. Moreover, secret keys, coffidential data or security related code are not planned to be examined, extracted, stolen or corrupted. Even if legal actions such as patenting and cyber crime laws are in place, these techniques remain a significant threat to software developers and security expert.

This paper provides a survey on software protection and related areas which would encourage further research. This paper also provides a number of viewpoints, discuss challenges and suggest future directions.

II. LITERATURE SURVEY

Piracy, reverse engineering and tampering have been the major software threats. Collberg et al. [1] provided a compact outline of the approaches to protect against these threats. Software watermarking for instance focuses on protecting software reactively against piracy. It usually implants hidden, distinctive data into an application in such a way that it can be guaranteed that a particular software instance belongs to a particular individual or company. When this data is distinctive for each example, one can mark out copied software to the source unless the watermark is smashed. The second group, code obfuscation, protects the software from reverse engineering attacks. This approach comprises of one or more program alterations that alter a program in such a way that its functionality remains identical but analyzing the internals of the program becomes very tough. A third group of approaches focuses to make software "tamper-proof", also called tamper-resistant.

Protecting the reliability of software platforms, particularly in unmanaged customer computing systems is a tough task. Attackers may try to carry out buffer

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overflow attacks to look for the right of entry to systems, steal secrets and patch on the available binaries to hide detection. Every binary has intrinsic weakness that attackers may make use of. In this paper Srinivasan et al., [3] proposed three orthogonal techniques; each of which offers a level of guarantee against malware attacks beyond virus detectors. The techniques can be incorporated on top of normal defenses and can be integrated for tailoring the level of desired protection. The author tries to identify alternating solutions to the issue of malware resistance. The techniques used are adding diversity or randomization to data address spaces, hiding significant data to avoid data theft and the utilization of distant evidence to detect tampering with executable code.

This paper focuses on the protection of a software program and the content that the program protects. There have been billions of dollars spent each year by the industries especially for software piracy and digital media piracy. The achievement of the content/software security in a huge segment is based on the ability of protecting software code against tampering and identifying the attackers who issue the pirate copies. In this paper, Hongxia Jin et al., [4] concentrates on the attacker identification and forensic examination. The author discussed about a proactive detection approach for defeating an on-going attack before the cooperation has occurred. The author also describes another detection approach for postcompromise attacker identification. Especially, the author takes into account the real world scenarios where the application programs connect with their vendors every so often, and where a discovery of attacking can bar a hacker user from further business.

Code obfuscation focuses to protect code against both static and dynamic study and there exists another approach to protect against code analysis, namely self-modifying code. This approach provides the opportunity to create code at runtime, rather than changing it statically. Practically, self-modifying code is highly restricted to the monarchy of viruses and malware. Yet, some publications regard self-modifying code as an approach to protect against static and dynamic analysis. Madou et al., [5] for instance regard dynamic code generation. The author proposed an approach where functions are generated earlier to their first call at runtime. Moreover, clustering is presented in such a way that a general template can be utilized to generate each function in a cluster, carrying out a least amount of alterations. In order to protect the constant `edits' against dynamic analysis, the authors suggested the usage of a Pseudo Random Number Generator (PRNG). The decryption at runtime technique is equal with code generation, apart from the fact that the decryption key can depend on other code, rather than on a PRNG. Moreover, it lessens re-encryption the viability of code during execution, while Madou et al. do

not clearly protect a function template after the function executed.

Protecting code against tampering can be regarded as the issue of data authenticity, where 'data' refers to the program code. Aucsmith [6] explained an approach to implement tamper resistant software. The approach protects against analysis and tampering. The author utilizes small, armored code segments, also called Integrity Verification Kernels (IVKs), to validate code integrity. These IVKs are protected via encryption and digital signatures in such a way that it is tough to modify them. Morover, these IVKs can communicate with each other and across applications via an integrity verification protocol.

Chang et al. [7] proposed an approach depending on software guards. The protection technique of the author is chiefly based on a composite network of software guards which mutually validate each other's consistency and that of the program's critical sections. A software guard is a small segment of code carrying out particulars tasks, e.g. check summing or repairing. When check summing code discovers a modification, repair code is capable to undo this malevolent tamper challenge. The security of the approach depends partly on hiding the obfuscated guard code and the complexity of the guard network.

Horne et al. [8] described on the same idea of Chang et al. [7] and proposed `testers', small hashing functions that validate the program at runtime. These testers can be integrated with embedded software watermarks to result in a unique, watermarked, selfchecking program. Other related research unconscious hashing [9] which interweaves hashing instructions with program instructions and which is capable of proving whether a program is operated correctly. Recently, Ge et al. [10] presented a research work on control flow based obfuscation. Although the authors contributed to obfuscation, the control flow data is protected with an Aucsmith-like tamper resistance approach.

Buffer overflow utilization is a one of the notable threat to software security. In order to lessen the threat, Visual studio C/C++compiler facilitates to randomize the addresses of the compiled program in initialization time and to implant security stack guards by the compiled program in run time. Yongdong Wu [11] upgrades the compiler by raising the compiled program's abilities in the following features:

- i. Protects a frame pointer from tampering without additional cost;
- ii. Defeats the attack which tampers 1-2 bytes of a protected region at a very low cost;
- iii. Checks the indirect function call against the prologue pattern so as to lessen the probability of software crash in case of being attacked.

The experiments demonstrated the enhancement on Microsoft Visual Studio in generating secure and robust software.

Cappaert et al., [12] presented a partial encryption approach depending on a code encryption approach [12], [13]. In order to utilize the partial encryption approach, binary codes are partitioned into small segments and encrypted. The encrypted binary codes are decrypted at runtime by users. Thus, the partial encryption overcomes the faults of illuminating all of the binary code at once as only the essential segments of the code are decrypted at runtime.

Jung et al., [14] presented a code block encryption approach to protect software using a key chain. Jung's approach uses a unit block, that is, a fixed-size block, rather than a basic block, which is a variable-size block. Basic blocks refer to the segments of codes that are partitioned by control transformation operations, such as "jump" and "branch" commands, in assembly code [12], [13]. Jung's approach is very similar to Cappaert's scheme. Jung's approach tries to solve the issue of Cappaert's approach. If a block is invoked by more than two preceding blocks, the invoked block is duplicated.

Unauthorized reverse-engineering of algorithms is a major issue for the software industry. Reverseengineers look for security holes in the program to make use of competitors' vital approaches. In order to discourage reverse-engineering, developers use a wide range of static software protections to obfuscate their programs. Metamorphic software protections include another layer of protection to conventional static obfuscation approaches, forcing reverse-engineers to alter their attacks as the protection changes. Program fragmentation incorporates two obfuscation approaches, over viewing and obfuscated jump tables, into a novel, metamorphic protection. Segments of code are eliminated from the chief program flow and placed throughout memory, minimizing the locality of the program. These fragments move and are called using obfuscated jump tables which makes program execution hard. This research by Birrer et al., [15] evaluates the performance overhead of a program fragmentation engine and offers examination of its efficiency against reverse-engineering approaches. The experimental results show that program fragmentation has low overhead and is an effective approach to obscure disassembly of programs through two common disassembler/debugger tools.

Song-kyoo Kim [16] deals with the stochastic maintenance approach for the software protection through the closed queueing system with the untrustworthy backups. The technique shows the theoretical software protection approach in the security viewpoint. If software application modules are denoted as backups under proposed structural design, the system can be overcome through the stochastic maintenance model with chief untrustworthy and random auxiliary spare resources with replacement strategies. Additionally, the practical approach of technology improvement in software engineering through the technology innovation tool called TRIZ.

Zeng Min et al., [17] considered the supple manufacturing venture networks data security and software protection and proposed an enterprise classified data security and software protection solution, to describe the enterprise data storage, transmission and application software installation authorization, license and so on, presented a time and machine code depending on MD5, AES encryption algorithm dynamic secret key the encryption approach, to protect the enterprise data confidentiality, integrity and availability, to attain the software installation restrictions and using restrictions.

Kent [18] proposed a software protection technique which deals with the security needs of software vendors like protection from software copying and modification (e.g. physical attacks by users, or program-based attacks). Techniques proposed to handle these requirements include physical Tamper-Resistant Modules (TRMs) and cryptographic techniques. One approach comprises of using encrypted programs, with instructions decrypted immediately preceding to execution. Kent also observed the dual of this issue like user needs that externallysupplied software be confined in its access to local resources.

Gosler's software protection survey [19] investigates circa-1985 protection technologies which comprise of hardware security tools (e.g. dongles), floppy disc signatures (magnetic and physical), analysis denial approaches (e.g. anti-debug approaches, checksums, encrypted code) and slowing down interactive dynamic analysis. The main goal is on software copy prevention, but Gosler observed that the potency of resisting copying should be balanced by the potency of resisting software analysis (e.g. reverse engineering to study where to alter software and for protecting proprietary approaches) and that of software modification (to bypass security checks). Useful tampering is generally headed by reverse engineering.

Gosler also described that one should anticipate that an opponent can execute dynamic analysis on the target software without discovery (e.g. using in-circuit emulators and simulators) and that in such scenario, due to repeated experiments, one should anticipate the opponent to win. Thus, the main goal of practical resistance is to construct such experiments "enormously arduous". Another proposal [19] is cycling software (e.g. through some forced obsolescence) at a rate faster than an opponent can break it; this expects the model of forced software renewal (Jakobsson and Reiter [20]), who suggested hopeless pirates via forced updates and software aging). This technique is suitable

where protection from attacks for a restricted time period suffices.

Herzberg and Pinter [21] focused on the issue of software copy protection and presented a solution needing CPU encryption support (which was far less possible when presented almost 20 years ago, circa 1984-85). Cohen's research [22] on software diversity and obfuscation is directly concentrated to software protection and offers a wide range of algorithms.

The subsequent practical tamper resistance system of Aucsmith [23] handled similar problems by an integration of just-in-time instruction decryption, and rearranging instruction blocks at run-time to vigorously change the deals with the executing statements during program execution.

Several researchers have proposed techniques on software obfuscation using automated tools and code transformations [24, 25]. One idea would be to employ language-based tools to transform a program (most easily from source code) to a functionally equivalent program which presents greater reverse engineering barriers. If implemented in the form of a precompiler, the usual portability issues can be handled by the back-end of standard compilers.

Collberg et al. [26] provides more information regarding software obfuscation which includes descriptions about:

- Categorizing code transformations (e.g. control flow obfuscation, data obfuscation, layout obfuscation, preventive transformations)
- Identification of contr@bw changes using opaque predicates (expressions not easy for an attacker to predict, but whose worth is recognized at compilation or obfuscation time)
- Preliminary suggestions on metrics for code transformations
- Program slicing tools
- The usage of (de)aggregation offlow control or data

Essential suggestions in software protection are done by Aucsmith [6], in combination with Graunke [23] at Intel. Aucsmith provides tamper prevention software which prevents inspection and change, and it is highly dependent to work accurately in unfriendly situations. Architecture is suggested according to an Integrity Verification Kernel (IVK) that checks the reliability of vital code segments. The IVK architecture is self-decrypting and includes self adjustment code.

Software tampering prevention using selfchecking code was described by Horne et al. [27]. The integrity of segments of code is tested using some code known as testers. This can be a linear hash function and a predictable hash value. If the integrity condition is not satisfied, suitable actions will be carried out so as to make the integrity condition satisfied. The attackers can be confused and it is difficult for them to hack the testers if more number of testers is used.

Chang and Atallah [28] presented a technique with fairly extensive capacity containing a set of guards that can be programmed to perform arbitrary processes. An illustration for this is the check sum code segments for integrity checking which provides resistance against software tamper. An additional described guard function is repairing code (e.g. if a spoiled code segment is identified, downloading and installing a new version of the code section). The author also presents a technique for automatically keeping protections within code.

Chen et al. [29] put forth oblivious hashing that engages compile-time code alterations which outcomes in the calculation of a running trace of the execution history of a complete code. In this approach a trace are considered as increasing hash values of a subset of expressions that happens inside the usual program execution.

Gutmann [30] put forth an apparent conversation of the security concerns facing cryptographic usage in software under general-purpose operating systems, and analyzes the design difficulties in nullifying these concerns faced by using secure cryptographic co-processors.

Approaches	Functionalities		
[1]	Outline of the approaches to protect against these threats. Software watermarking for instance focuses on protecting software reactively against piracy		
[2]	Proposed three orthogonal techniques; each of which offers a level of guarantee against malware attacks beyond virus detectors.		
[4]	Concentrates on the attacker identification and forensic examination. The author discussed about a proactive detection approach for defeating an on-going attack before the cooperation has occurred		
[5]	an approach in which functions are generated earlier to their first call at runtime		
[6]	The author utilizes small, armored code segments, also called Integrity		
	Verification Kernels (IVKs), to validate code integrity		
[7]	The protection technique of the author is chiefly based on a composite network of software guards which mutually validate each other's consistency and that of the program's critical sections.		
[12]	Presented a partial encryption approach depending on a code encryption approach		

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Presented a code block encryption		
approach to protect software using a		
key chain		
Deals with the stochastic		
maintenance approach for the		
software protection through the		
closed queueing system with the		
untrustworthy backups		
Focused on the issue of software		
copy protection and presented a		
solution needing CPU encryption		
support		
Software tampering prevention using		
self-checking code		

III. PROBLEMS AND DIRECTIONS

The theoretical results to date on software obfuscation provide software protection of considerable practical value. The impracticality of constructing a program to find out whether other software is malicious does not preclude highly valuable computer virus detection technologies, and a feasible, anti-virus industry. It is still early in the history of research in the domains of software protection and obfuscation and that several discoveries and innovations lie ahead particularly in the domains of software diversity (which are utilized are less in the present scenario), and software tamper resistance. Increased number of secure techniques for software protection is very much needed which involves public scrutiny and peer evaluation. Cappaert proposed a tamper-resistant code encryption scheme, and Jung proposed a key-chain-based code encryption scheme. However, Cappaert's scheme did not meet the security requirements for code encryption schemes, and Jung's scheme had an efficiency problem. Moreover, time cost and space cost should also be taken into consideration. To improve efficiency, support from the compiler and operating system is needed [19].

More open discussion of particular approaches is very much needed. Cryptography is observed to be the technique that can be incorporated in the software protection technique for improved protection. Past trends of proprietary, undislosed techniques of software obfuscation approaches similar to the early days in cryptography have to be altered.

For decades encryption has provided the means to hide information. In this research, the selfencrypting code is used as a means of software protection. In this research work, the concept of efficient code encryption techniques, which offers confidentiality and a method to create code dependencies that implicitly protect integrity need to be established. Moreover, several dependency schemes based on a static call graph which allow runtime code decryption simultaneous with code verification can also be used. If code is modified statically or dynamically, it will result in incorrect decryption of other code, producing a corrupted executable. Better and efficient cryptographic techniques can be integrated for better results. This research uses the encryption technique to secure software static analysis and tampering attacks.

IV. CONCLUSION

This paper presented and discussed a survey on the protection of software because of various Several software protection techniques attacks. available in the literature are analyzed and examined. The characteristic features of the existing algorithms are thoroughly investigated in this paper. This study would facilitate in development of efficient software protection techniques. Encryption techniques can be incorporated with the existing software protection techniques to improve the overall security of the software. Code encryption schemes for protecting software against various attacks like reverse engineering and modification. Therefore, novel and efficient code encryption scheme have to be established based on an indexed table to guarantee secure key management and efficiency.

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Broadcasting methods in mobile ad hoc networks: Taxonomy and current state of the art

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Abstract - Flooding also known as broadcasting is one of the most primitive methodologies that focus on investigating searches concerning mobile ad hoc networking due to poorer network procedures which is a main feature in the concept of broadcasting which provides implications to superior applications that includes routing. Broadcasting means in conventional ways transmitting messages from a given branch to all other branches present in a network. The whole grid of the network is manned to ensure that the transmitted data is uniformly ported to the remaining nodes in a decentralized type of network setup. The two issues that renders nodes out of reach all the time are limited radio range and their immovability which assists in concluding that te issue of data transmission covering all networks is assumed to be a multi-objective issue that aims at increasing the count of number of nodules and also decreasing the time taken to reach the specified nodules and also reducing the network overhead which is a crucial characteristic because of the fact that this may direct to congestion also known as broadcast storm issue. This article aims at giving an insight of the taxonomy of transmitting methodologies in MANETS and current state of the art.

GJCST Classification: C.2.2, C.2.4, C.2.6



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Broadcasting methods in mobile ad hoc networks: Taxonomy and current state of the art

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Abstract - Flooding also known as broadcasting is one of the most primitive methodologies that focus on investigating searches concerning mobile ad hoc networking due to poorer network procedures which is a main feature in the concept of broadcasting which provides implications to superior applications that includes routing. Broadcasting means in conventional ways transmitting messages from a given branch to all other branches present in a network. The whole grid of the network is manned to ensure that the transmitted data is uniformly ported to the remaining nodes in a decentralized type of network setup. The two issues that renders nodes out of reach all the time are limited radio range and their immovability which assists in concluding that te issue of data transmission covering all networks is assumed to be a multiobjective issue that aims at increasing the count of number of nodules and also decreasing the time taken to reach the specified nodules and also reducing the network overhead which is a crucial characteristic because of the fact that this may direct to congestion also known as broadcast storm issue. This article aims at giving an insight of the taxonomy of transmitting methodologies in MANETS and current state of the art.

I. INTRODUCTION

Mobile ad hoc network ensures building a provisional network sans the involvement of a recognized transportation or an integrated administration. MANETs are usually used for the common usage to emergency situations in warfields, rescue sites etc.

Every node present in MANET can be considered a router. The source node utilizes the intermediate nodes to transmit the message towards the destination node if a source node fails to transmit a message unswervingly to its destination node. MANET networks propose reliability, bandwidth and battery power and have erratic traits like topology. Strength signal and transmission routes. Transmission algorithms and procedures are supposed to be very light to save energy and bandwidth in computation and storage necessities [1, 2, 3, 4, and 5].

Routing information discovery is crucial for all MANET networks using standards such as dynamic source routing (DSR), ad hoc on demand distance vector (AODV), zone routing protocol (ZRP) and location aided routing (LAR) employ the procedure of transmission to launch routes, which can be achieved through the process of data transmission where sender sends a data packet to rest all branches present in MANET. Node mobility and limited system reserves pose serious issues in broadcasting MANETs as compared to wired networks.

II. RELATED WORK

Transmission standards have been categorized into 4 groups namely simple flooding, probability-based methods, area-based methods and neighbor knowledge based methods, in accordance with the fact that the branches should be in order so as to be implemented by Williams and Camp. Simple flooding involves forwarding received data packets by branches one at a time resulting in jamming of network. Probability related methodologies are typically appraised time and again whenever a packet reaches the destination node which happens with some specific probability. When some extra topographical locations are covered due to some emission process, then re-transmission can be expected where area and location related methods explain whether the facades or the span of the projected area is low or not and if yes, then the message is not GPS or estimation by the triangulation resent. procedure or calculation of power of radio signals hold basis for provision of network information. SBA, Flooding With Self-Pruning (FSWP), AHBP, Multi-point Relaying, etc are few neighbor knowledge related procedures which are essential to procure information of neighborhood neighbors like FWSP uses 1-hop neighbors, SBA, Multipoint Relaying and AHBP uses 2hops neighbors etc. They comprise the last category of transmission methodologies which are again segregated into 2 sub-divisions: neighbor-designating and self-pruning methods whose standard procedure themselves take a decision whether to retransmit the message or not while the former functions by activating its fellows wich are ready to relay a packet.

Stojmenovic and Wu introduced some classifications for transmitting standards which are dependent on their algorithmic nature or the data that is essential for its implementation (network information, "hello" messages content, broadcast messages content). When a transmitting algorithm works assuringly on a specified input, a confirmed output is always projected most of which are deterministic and is assumed to be secure only when all the noes [resent in

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the network are taken into consideration. Probabilistic schemes and area-based methods are almost always risky to rely upon due to the fact that they usually fail in terms of randomness and heuristics, respectively.

Wu and Lou introduced a concept stating the quantity of data needed for transmission and also classified standards based on whether they depend on areas including global, quasi-global, local or quasi-local knowledge of the prevalent network wherein global and quasi-global transmitting algorithms are known as centralized standards whose main disadvantage is they are not scalable and hence can be utilized in MANETs. There are few localized standards whose examples are 1 and 2-hops neighborhood standards whose network status information and its topology are exchanged between various branches which is transmitted either by some random "hello" message or transmitted messages whose data content lays down a grave collision on the network throughput ultimately.

 Broadcasting in Manets

 probablistic

 deterministic

 Distance based

 Counter based

 Location based

 Scalable

 Broacasting

 Cluster based

Making utmost use of the IEEE 802.11 MAC specifications, transmitting methodologies have been classified into four groups.

b) Statistical and geometrical model based broadcasting methods

Retransmitting of data packets ensue which involves every node in the simple flooding technique.

• Messages are distributed to all the neighboring nodes by a source node in MANET, the nodes will scan and check whether they have already seen the transmitted message and if yes, the packet is discarded and if not, t will again be re-initiated to all the potential nodes until the message reaches to every node present in the network. This methodology poses the issue of network jamming and weakening of battery power due to the presence of low concentration of nodes and high mobile power. If the messages constitute a polynomial number whose magnitude is (n2), it is of size n and is portrayed in the above diagram.

The topology of the prevalent network designs options for retransmitting of nodes based on probability standards.

- Probability Based Approach: This concept helps to identify and rectify the issues created due to the application of simple flooding methodology. A fixed probability pi for retransmission is assigned for each node I 2 N which involves lessening of the jamming circumstance and avoiding collisions. In situations when pi=1, then this concept turns itself towards simple flooding concept. There is a sufficient decrease in pi if there exists efficient transmission because of the increase and reduction in the count of neighbor density nodes.
- Counter-Based Scheme Approach: The random assessment delay (RAD) is posted, a threshold K is resolved and a counter k >= 1 is fixed on the basis of the count of the received transmitted message which in due course of RAD is increased considerably by one for every acknowledged message. The message is declined when RAD terminates and k >= 1. Few nodes won't be permitted to re-transmit in an opaque MANET while in a less intense MANET, all nodes will retransmit the messages.

An area related common broadcasting span is presumed and a node retransmits the message if there is a provision of adequate coverage location. Span and area based approaches are included in the methodologies mentioned below which can be explained as follows:

Distance Based Approach: The counter is made use of in the counter based approach to decline or retransmit a message wherein here, the span concerning the source and the destination node will be chosen by them both, say suppose the span is d. if the value of d is small, the retransmitting coverage span is less and if d is large enough, then the coverage will also be large but if d=0, then the coverage value is 0. The threshold span D is established by a receiving nodule and then RAD is preset where superfluous messages will be preserved until RAD is terminated. Now if d < D. then received transmitted messages will be declined else they'll be retransmitted again. It has been proposed by Ni et al that signal strengths are made use of to estimate the span starting from the source nodule. Span is capable enough to restore signal power by handling the signal threshold.

Location Based Approach: This is concerned with every nodule covering the basic need of instituting self sites for calculating extra coverage more clearly which is based on the global positioning system (GPS). Every node in MANET takes care in attaching its self area to the header part of every message that it is sending or retransmitting. The location of the sender is first deemed and extra coverage span is estimated where the message is declined in case RAD terminates when span area is considerably less than the given threshold. The price of estimating extra coverage spans, also considering estimations of intersections among

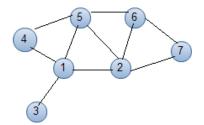
a) Taxonomy of broadcasting methods in mobile ad hoc networks

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circles is a setback for this approach which can consume the inadequate energy currently available.

Neighborhood based: Status that is prevalent in the neighborhood is managed by the same method wherein matter received from the fellow nodes is employed for retransmission.

Self Pruning: Every node present in this feature is supposed to be aware of who its neighbors are, which can be attained by episodic messages. The receiving nodule evaluates with the source's list as to who all its neighbors are and may retransmit if extra nodes are within reach, else they will al be declined. Figure below depicts retransmission of message from node 2 to node 1 which retransmits them to node 3 and 4 respectively as they are extra nodes and so does node 5 with node 4. Idleness is rampant here even under such circumstances.

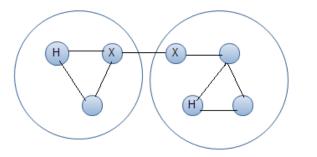


- Scalable Broadcasting Approach: Self pruning is enhanced here as there exist higher opportunities for message broadcasting later and also is supposed to make sure that all nodes present in the network are aware of their neighbors until the 2 hop span as each and every node in this methodology consists of a two-hop topology information, which is instituted by "Hello" messages.
- Ad Hoc Broadcasting Approach: This methodology permits only nodules chosen as gateway nodules and a transmission message controller to retransmit the message. It can be explained in detail as follows:
 - 1. Choose one hop fellow nodule as gateway from amongst all the two hop neighboring nodes that can be accessible to a one hop fellow node.
 - 2. Estimate cover set which receives message from the presently in use gateway set.
 - 3. Chose and fix a one hop fellow node as gateway which can cover almost every two hp neighbor that's not present in the cover set.
 - 4. Continue the above processes 2 and 3 till all two hop fellow nodules are enclosed.
 - 5. The nodule which receives a message and is known to be a gateway, decides on which of its fellow nodules have already accepted delivery of the message, they are then presumed to be covered and is declined by the fellow nodule to chose the subsequent hop gateways.

c) Cluster Based Broadcasting Methods

Data traffic organization schemas, routing severities, fault tolerance problems are few topics of concern for which clustering approach is considered.

For enlightening its existence, every nodule sends timely "Hello" messages and possesses an exclusive ID. A group of nodes is collectively known as a cluster which can be fashioned as follows: A nodule possessing a restricted negligible IT will nominate self as the cluster in-charge within where a gateway is utilized for transmission between two members of different clusters. In case the two in-charge heads come together, the nodule with the bigger ID status sacrifices its head position. Cluster configuration can be portrayed as follows:



X is a gateway here while Y is the in-charge head.

d) Tree Based broadcasting Methods

This methodology is usually not preferable and termed as unsuitable for MANETs even though transmission with the help of tree techniques in wired networks is a famous and technique that is in use too often as they portray a drastic and a powerful transformation in network topologies.

e) Technical challenges

This segment focuses on few issues of importance that are to be tackled during the tenure of outlining transmission standards.

f) Hidden and exposed node problems

This issue is of a major concern during transmission as it renders the transport of nodes to all locations unfeasible concerning a given phase of network subsets. This is supposed to be rectified by the usage of acknowledgement packets (ACKs) but this would be against the rule of transmission that specifies to reduce the count of data packet production. Subsequently, the main principle of transmission within a static ad hoc network is to avail to as many nodes that are within reach. A transmitting standard may be unsuccessful even at the slightest hint of non-availability of acknowledgement packets.

g) Mobility and Partitioning

Mobility is another important issue of concern, the transmitting standards are supposed to face.

Transmission of messages on the basis of spanning trees endures problems posed by the mobility factor, which can now be dealt with expertly because of the availability of many equipped algorithms that are competent enough. These methodologies also are not specific on which application they want to work with. Hence, considering transmission tends to give an idea as to how the corresponding spanning tree is supposed to be built, which is done by exploring group of relevant nodes that have already been recipients of the sender.

This issue in static ad hoc networks can also unwillingly pave way to separated networks which is another issue of major concern, taking broadcasting in particular. A transmitting standard is supposed to be searching evidently for a methodology which can permit a transmitting message to leap to various subsets, so as to cover as many nodules as it is permitted. Epidemic dispersive replicas can be correlated with this concern.

h) Frequently cited broadcasting methods literature

The cluster that materializes in MANET is supposed to be preserved on a regular basis as said by Ni et al by the fundamental cluster algorithm. All the remaining nodules in a cluster other than the controlling nodule can be enveloped by the retransmission done by the controller. Gateway nodes are extensively utilized for retransmitting message to other nodules in various clusters and hence there is no extreme necessity of a non-gateway nodule to retransmit the same message. Even though there may be presence of many gateway nodules in various clusters, te specified gateways may probably utilize and employ different transmission concepts mentioned earlier to ascertain whether to retransmit or not.

Spanning trees are widely built though which transmission of messages is done by promoting messages only to the fellow nodules present in the tree which is basically acyclic in nature. Hence, every transmitted message is acquired only once at a time by the prevailing nodes. Many algorithms are available for building and upholding trees like the bridged Ethernet network's spanning tree algorithm which are built to prove suitable for working in stable networks rather than the ever-changing topology of a MANET.

Multicast trees and their uses have already been specified and explained in detail by many authors but what is nagging is the fact their algorithms are not qualified and efficient enough for dealing with the topology related modifications. Few algorithms render their services useless for handling the tree in an everchanging topology even though they may involve a phase of building a spanning tree.

The logic of one-to-one broadcasting is proved feasible by the tree related technique as compared to other methods as many disadvantages of limited transmissions do not influence the algorithm thankfully, it is secure enough for transmissions. A noteworthy point to mention is there is minimum or no effort required to be at par with the network states as it has been proposed from the start to reduce signaling traffic.

i) Current state of the art

A multiple channel medium access control (MAC) standard was recommended by **Jenhui Chen et al** which was named as ad hoc multichannel negotiation protocol (AMNP) used mainly for transmitting messages across multiple channels in a uniform manner and also referred to the problem of distributed scheme allowance for multihop MANETs but in the presence of one transceiver. Augmentation of description of AMNP known as AMNP with channel development was brought into existence.

The replication results prove worthy to make its stand that the throughput is comparatively large in comparison to its single path equivalent. Only a single transceiver is deployed by the recommended AMNP but with a specified constraint of suspending right to admission for a specific time period while getting swapped to a chosen data route.

It has been suggested by Chien-Chung Shen et al, a diagram-prospect related directional to curve percolation and also for omni-directional transmission for spot percolation and also gives a detailed explanation about the compilation of directional transmitter related transmission methods for static ad hoc networks. The author squabbles to support the stipulation of suggested copy, that countless transmission designs have been recommended almost all of which presumed the practice of omni-directional transmitters and transmission overhead is taken into consideration advancing number of dispatching nodules. Directional transmitters possess tapered emissions and can gradually diminish transmission overheard with respect to number of acknowledged packets to the count of nodules that expects transmission packets.

Observation: It has been suggested that diagram-prospect related directional to curve percolation and also for omni-directional transmission for spot percolation and also gives a detailed explanation about the compilation of directional transmitter related transmission methods for static ad hoc networks. A specific quantity of battery power is safeguarded by decreasing the count of replica of data packets that is acknowledged. For utilizing the longer range characteristics of directional transmitters for deceasing the delay, it is but essential to scrutinize the recommended ideas.

Transmission storm issue would be a troubling factor if the accelerating nodules are not cautiously allocated in the transmission procedure in static ad hoc networks (MANETs) said Wei Lou et al. The main idea behind diminishing transmission idleness is a foremost issue of concern in MANETs and so an easy transmission algorithm has been recommended known as double-covered broadcast (DCB), which benefits from the transmission idleness state for enhancing delivery ratio in a high broadcasting error rate surrounding subset.

Few chosen promoting nodules rebroadcast the transmitted message from among the 1-hop fellow nodules pertaining to the sender. The above mentioned nodules are chosen as follows:

- 1) 2-hop fellow nodules of the source initiator are swathed and
- The source initiator's 1-hop fellow nodules are either advancing nodules or non advancing ones that are enveloped by a minimum of two promoting fellow nodules.

The source initiator acquires hold of the rebroadcasts of the promoting nodules as authentication of reaction of the data packet. The non promoting 1-hop fellow nodules of the source initiator fail to admit the response reaction of their transmission which provokes the initiator to retransmit the data packet in case of it failing to recognize all the promoting nodules broadcast till large count of retries has been reached.

Observation: Suppose in DCB, a nodule v promotes a data packet choosing division of the prevalent 1-hop fellow nodules as promoting nodules depending on the greedy algorithm for the issue pertaining to Set Cover with certain restrictions which are as follows: (1) All the 2-hop fellow nodules of node v are supposed to be addressed by the chosen advancing nodes and (2) the 1-hop fellow nodules available in node v can either be chosen as an advancing nodule r enveloped by a minimum of two promoting nodes. Then, the IDs of the chosen promoting nodules to the relevant data packet is appended to the node v and then the same packet is transmitted. It is assumed beforehand by a 1-hop fellow node that the about-to-be received data packet is of a promoting nature and transmission procedure continues as it was before concerning node v. Another noteworthy characteristic of DCB is that whenever a nodule tends to broadcast a packet secure connections are ensured. Node v delays time to eavesdrop on transmission from all its chosen promoting nodules and if it falls short for the same, then rebroadcasting is permitted until all nodules are swathed and maximum tries are attained.

j) Limits and obstacles observed in Existing Broadcasting Methods

The disadvantages construed from itemizing relative studies are as follows:

1. There is a need for many retransmissions concerning with the count of the rebroadcasting nodules for all methodologies explained except for the neighbor related techniques.

- 2. RAD implemented techniques drowned in high density MANETs acclimatize RAD nodules concerning its surrounding conduct is cultivated.
- 3. The ad hoc transmitting technique faces discrepancies in a typically high static MANET network because of the fact that it fails to make use of local data to confirm whether to retransmit the packet or not.

On the basis of the wide proportional research on already existing transmitting techniques, it has been observed that every transmitting technique failed to work in wide ranging MANET surroundings.

Scalable transmission based line of attack has provided with noteworthy and promising results as compared to the non-adaptive tactics.

There is an urgent need to cultivate new competent data transmission tactics with the main intention of preserving the existing meager reserves in MANETs.

vii. Conclusion

Transmission is an indispensible feature for any MANET network, so it is vital to exploit the most proficient transmitting techniques that can make sure that a secure network is provisioned. This paper also has presented a brief synopsis on all chief transmission methodologies available in the prose, mainly concentrating on the intricacies of their roles and also the threats posed by them and also, recommending upgrading for few of the enumerated techniques. There is not one most favorable algorithm in existence for all the concerning techniques in the present circumstances even though a vigorous change is pertinently visible in the MANET topology and its rarely obtainable reserves.

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An Enhanced Cuckoo Search for Optimization of Bloom Filter in Spam Filtering

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GJCST Classification: D.3.4 , G.1.6, B.1.4

AN ENHANCED CUCKOD SEARCH FOR OPTIMIZATION OF BLOOM FILTER IN SPAM FILTERING

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An Enhanced Cuckoo Search for Optimization of Bloom Filter in Spam Filtering

Arulanand Natarajan ^{α}, Subramanian S^{Ω}, Premalatha K^{β}

Abstract - Bloom Filter (BF) is a simple but powerful data structure that can check membership to a static set. The tradeoff to use Bloom filter is a certain configurable risk of false positives. The odds of a false positive can be made very low if the hash bitmap is sufficiently large. Spam is an irrelevant or inappropriate message sent on the internet to a large number of newsgroups or users. A spam word is a list of well-known words that often appear in spam mails. The proposed system of Bin Bloom Filter (BBF) groups the words into number of bins with different false positive rates based on the weights of the spam words. An Enhanced Cuckoo Search (ECS) algorithm is employed to minimize the total membership invalidation cost of the BFs by finding the optimal false positive rates and number of elements stored in every bin. The experimental results have demonstrated for CS and ECS for various numbers of bins.

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I. INTRODUCTION

A spam filter is a program that is used to detect unsolicited and unwanted email and prevent those messages from getting into user's inbox. A spam filter looks for certain criteria on which it stands decisions. For example, it can be set to look for particular words in the subject line of messages and to exclude these from the user's inbox. This method is not effective, because often it is omitting perfectly legitimate messages and letting actual spam through. The strategies used to block spam are diverse and includes many promising techniques. Some of the strategies like black list filter, white list /verification filters rule based ranking and naïve bayesian filtering are used to identify the spam.

A BF presents a very attractive option for string matching (Bloom 1970). It is a space efficient randomized data structure that stores a set of signatures efficiently by computing multiple hash functions on each member of the set.

It queries a database of strings to verify for the membership of a particular string. The answer to this query can be a false positive but never be a false negative. The computation time required for performing

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the query is independent of the number of signatures in the database and the amount of memory required by a BF for each signature is independent of its length (Feng et al 2002).

This paper presents a BBF which allocates different false positive rates to different strings depending on the significance of spam words and gives a solution to make the total membership invalidation cost minimum. BBF groups strings into different bins via smoothing by bin means technique. The number of strings to be grouped and false positive rate of each bin is identified through GA which minimizes the total membership invalidation cost. This paper examines different number of bins for given set of strings, their false positive rates and number of strings in every bin to minimize the total membership invalidation cost.

The organization of this paper is as follows. Section 2 deals with the standard BF. Section 3 presents the CS technique. Section 4 reports optimized BBF using ECS. Performance evaluation of CS and ECS for the BBF is discussed in section 5.1

II. BLOOM FILTER

Bloom filters (Bloom 1970) are compact data structures for probabilistic representation of a set in order to support membership queries. This compact representation is the payoff for allowing a small rate of false positives in membership queries which might incorrectly recognize an element as member of the set.

Given a string S the BF computes k hash functions on it producing k hash values and sets k bits in an m-bit long vector at the addresses corresponding to the k hash values. The value of k ranges from 1 to m. The same procedure is repeated for all the members of the set. This process is called programming of the filter. The query process is similar to programming, where a string whose membership is to be verified is input to the filter. The bits in the m-bit long vector at the locations corresponding to the k hash values are looked up. If at least one of these k bits is not found in the set then the string is declared to be a nonmember of the set. If all the bits are found to be set then the string is said to belong to the set with a certain probability. This uncertainty in the membership comes from the fact that those k bits in the m-bit vector can be set by any other n-1 members. Thus finding a bit set does not necessarily imply that it was set by the particular string being queried. However, finding a bit not set certainly implies that the string does not belong to the set. In order to store a given element into the bit array, each hash function must be applied to it and, based on the return value r of each function (r1, r2, ..., rk), the bit with the offset r is set to 1. Since there are k hash functions, up to k bits in the bit array are set to 1 (it might be less because several hash functions might return the same value). Figure 1 is an example where m=16, k=4 and e is the element to be stored in the bit array.

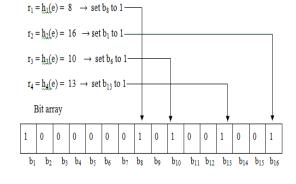


Fig.1 : Bloom Filter

One important feature of BF is that there is a clear tradeoff between the size of the filter and the rate of false positives. The false positive rate of BF is

$$f = (1 - e^{-kn/m})^{k} = \exp(k \ln(1 - e^{-kn/m}))$$
(1)

Let $g = k \ln(1 - e^{-kn/m})$. Minimizing the false positive probability f is equivalent to minimizing with respect to k.

$$\frac{dg}{dk} = \ln \left(1 - e^{-\frac{kn}{m}} \right) + \frac{kn}{m} \frac{e^{-kn/m}}{1 - e^{-kn/m}}$$
(2)

The derivative equals 0 when kmin=(1n2)(m/n). In this case the false positive probability f is:

$$f(k_{\min}) = (1-p)^{k_{\min}} = (\frac{1}{2})^{k_{\min}} = (0.6185)^{m/n}$$
 (3)

of course k should be an integer, so k is $[\ln 2 \cdot (m/n)]$

The BF has been widely used in many database applications (Mullin 1990; Mackert and Lohman, 1986). It is applied in networking literature (Brooder and Mitzenmacher, 2005). A BF can be used as a summarizing technique to aid global collaboration in peer-to-peer networks (Kubiatowicz et al., 2000; Li et al, 2002; Cuena-Acuna et al, 2003). It supports probabilistic algorithms for routing and locating resources (Rhea and Kubiatowicz 2004; Hodes et al,2002; Reynolds and Vahdat, 2003; Bauer et al, 2004) and share Web cache information (Fan et al,2000). BFs have great potential for representing a set in main memory (Peter and Panagiotis, 2004) in stand-alone applications. BFs have been used to provide a

probabilistic approach for explicit state model checking of finite-state transition systems (Peter and Panagiotis, 2004). It is used to summarize the contents of stream data in memory (Jin et al, 2004; Deng and Rafiei, 2006), to store the states of flows in the on-chip memory at networking devices (Bonomi et al, 2006), and to store the statistical values of tokens to speed up the statistical-based Bayesian filters (Li and Zhong, 2006). The variations of BFs are compressed Bloom filters (Mitzenmacher, 2002), counting Bloom filters (Fan et al,2000), distance-sensitive Bloom filters (Kirsch and Mitzenmacher,2006), Bloom filters with two hash functions (Kirsch and Mitzenmacher, 2006), spacecode Bloom filters (Kumar et al,2004), spectral Bloom filters (Cohen and Matias, 2003), generalized Bloom filters (Laufer et al, 2005), Bloomier filters (Chazelle et al, 2004), and Bloom filters based on partitioned hashing (Hao et al,2007).

III. CUCKOO SEARCH

Cuckoo search is an optimization algorithm inspired by the brood parasitism of cuckoo species by laying their eggs in the nests of other host birds proposed by Yang and Deb (2009). If a host bird discovers the eggs are not their own, it will either throw these foreign eggs away or simply abandon its nest and build a new nest elsewhere. Each egg in a nest represents a solution, and a cuckoo egg represents a new solution. The better new solution (cuckoo) is replaced with a solution which is not so good in the nest. In the simplest form, each nest has one egg. When generating a new solution Levy flight is performed. The rules for CS are described as follows:

- Each cuckoo lays one egg at a time, and dumps it in a randomly chosen nest
- The best nests with high quality of eggs will carry over to the next generations;
- The number of available host nests is fixed, and a host can discover an foreign egg with a probability p_a∈[0, 1]. In this case, the host bird can either throw the egg away or abandon the nest so as to build a completely new nest in a new location

The algorithm for CS is given below:

Generate an initial population of n host nests;

while (t<MaxGeneration) or (stop criterion)

Get a cuckoo randomly (say, i) and replace its solution by performing Levy flights;

Evaluate its fitness Fi

Choose a nest among n (say, j) randomly;

if (Fi > Fj),[for maximization]

Replace j by the new solution;

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A fraction (pa) of the worse nests is abandoned and new ones are built;

Keep the best solutions/nests;

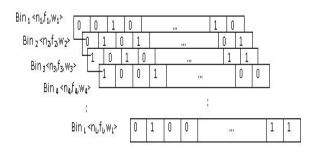
Rank the solutions/nests and find the current best;

Pass the current best to the next generation; end while

IV. ENHANCED CUCKOO SEARCH FOR BLOOM FILTER OPTIMIZATION

a) Bin Bloom Filter (BBF)

A BBF is a date structure considering weight for spam word. It groups spam words into different bins depending on their weight. It incorporates the information on the spam word weights and the membership likelihood of the spam words into its optimal design. In BBF a high cost bin lower false positive probability and a low cost bin has higher false positive probability. The false positive rate and number of strings to be stored is identified through optimization technique GA which minimize the total membership invalidation cost. Figure 2 shows Bin BF with its tuple <n,f,w> configuration.





b) Problem Definition

Consider a standard supervised learning problem with a set of training data $D = \{ \langle Y1, Z1 \rangle, \dots, \}$ <Yi, Zi>, ... ,< Yr ,Zr >} , where Yi is an instance represented as a single feature vector, Zi = C(Yi) is the target value of Yi, where C is the target function. Where Y1, Y2, ..., Yr set of text document collection C is a class label to classify into spam or legitimate (nonspam). The collection is represented into feature vector by the text documents are converted to normalized case, and tokenized them, splitting on non-letters. The stop words are eliminated. The spam weights for words are calculated from the set. This weight value indicates its probable belongings to spam or legitimate. The weight values are discretized and assigned for different Bins. The tuple to describe the Bin BF is, $\{\{n1, n2, ..., \}$ nL}, {w1, w2,..., wL}, m, {k1, k2, ..., kL}, {f1, f2, ..., fL}}. It is an optimization problem to find the value of n and f that to minimize the total membership invalidation cost. For membership testing the total cost of the set is the sum of the invalidation cost of each subset. The total membership invalidation cost (Xie et al., 2005) is given as,

$$F = n1f1w1 + n2f2w2 + \dots + nLfLwL$$

 $\sum_{i=1}^{L} n_i = N$

The total membership invalidation cost

$$\mathsf{F}(\mathsf{L}) = \sum_{i=1}^{\mathsf{L}} n_i \, \mathsf{w}_i \mathsf{f}_i \tag{4}$$

to be minimized.

Where

N- Total number of Strings in a spam set.

$$\mathbf{f}_{i} = \left(\frac{1}{2}\right)^{\ln 2 \times \left(r_{i}m / \sum_{j=1}^{i} n_{j}r_{j}\right)}$$
$$r_{i} = \ln(f_{i}) \quad (1 \le i \le L)$$

The objective function f(L) taken as standard for the problem of minimization is

$$f(L) = \begin{cases} C_{max} - F(L) & \text{if } F(L) < C_{max} \\ 0 & \text{if } F(L) \ge C_{max} \end{cases}$$
(5)

where Cmax is a large constant.

c) ECS for Optimization of BF

The CS is extended to an ECS in which each nest has multiple eggs representing a set of solutions. Generate an initial population of n host nests with m eggs;

while (t<MaxGeneration) or (stop criterion)

Get a cuckoo randomly (say, i) by Levy flights using the best egg in the chosen nest;

Evaluate its fitness Fi

Choose a nest among n and choose an egg with the worst solution in the nest (say, j);

if (Fi > Fj),[for maximization]

Replace j by the new solution i;

end if

Find the best solution (among m) in each nest;

Rank the nests based on the best solution;

Abandon a fraction (pa) of the nests which have worse solutions and built new ones;

Keep the best solutions/nests;

Rank the solutions/nests and find the current best; end while

When generating new solutions x(t+1) for a cuckoo i, a Levy flight is performed using the following equation (6)

$$\mathbf{x}_{i}^{(t+1)} = \mathbf{x}_{i}^{(t)} + \mathbf{a} \oplus \text{Levy}(\lambda)$$
(6)

The symbol \oplus is an entry-wise multiplication. Basically Levy flights provide a random walk while their random steps are drawn from a Levy distribution for large steps

$$Levy \sim u = t - \lambda \tag{7}$$

which has an infinite variance with an infinite mean. Here the consecutive jumps of a cuckoo essentially form a random walk process which obeys a power-law step- length distribution with a heavy tail. The representation of egg (solution) is given in figure 3.



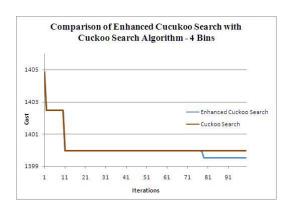
Fig.3 : Egg representations for Bin Bloom Filter

where nij, fij and wij refer respectively the number of words, false positive rate of and the weight of the jth bin of ith egg. The triplet $\langle n, f, w \rangle$ encodes a single bin. The false positive rate fij can be obtained from equation (1) where nij is drawn from the ith egg in the nest, m is known in advance and k is calculated from equation (3). One egg in the nest represents one possible solution for assigning the triples $\langle n, f, w \rangle$. At the initial stage, each egg randomly chooses different $\langle n, f, w \rangle$ for L Bins based on the given constraints. The fitness function for each egg can be calculated based on the equation (5).

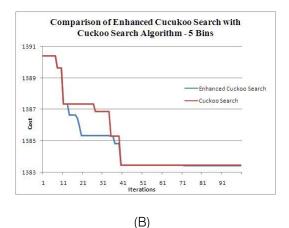
VII. EXPERIMENTAL RESULTS

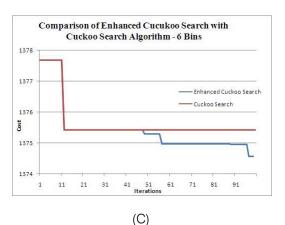
Cuckoo Search employs Levy flight for finding new solutions from equation (7). CS and ECS consider 10 nests and 50 iterations. The parameters pa, α and λ are set as 0.3, 1 and 1.5 respectively. The total number of strings taken for testing is 250, 500, and 1000. The string weights are varying from 0.0005 to 5. The size of the BF is 1024. This experimental setup is applied for number of bins from 4 to 7.

Figures 4a, 4b, 4c and 4d correspondingly show the total membership invalidation cost obtained from BBF for bin sizes from 4 to 7 for 1000 strings using CS and ECS algorithm. In this experimental setup the ECS performs better than CS. Figures 5a, 5b, 5c and 5d show the total membership invalidation cost obtained from BBF for bin sizes from 4 to 7 respectively for 500 strings. Figures 6a, 6b, 6c and 6d show the cost of BBF from bin sizes 4 to 7 for 250 strings. For all the string sizes the ECS outperforms CS. In CS, 10 nests which equals to number of nests in ECS and 40 nests which equals to number of eggs in ECS are taken to find the total membership invalidation cost for 1000 strings. Figure 7 shows the total membership invalidation cost obtained from BBF for the bin sizes ranging from 4 to 10 using CS and ECS. It shows that the cost is decreased when the numbers of bins are increased. The results obtained from ECS outperform CS for all bin sizes from 4 to 10.





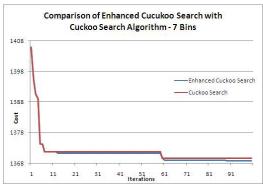




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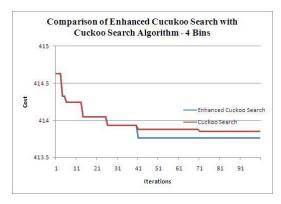
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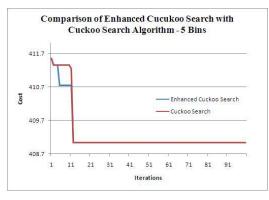


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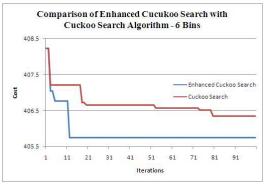




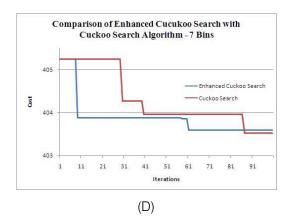




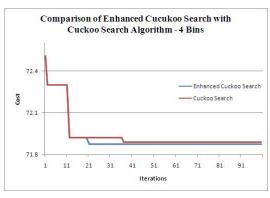
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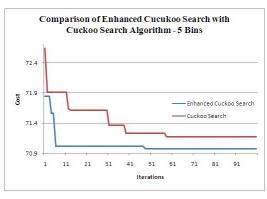




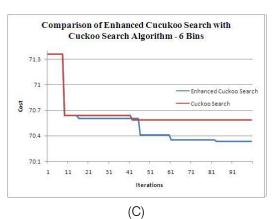








(B)



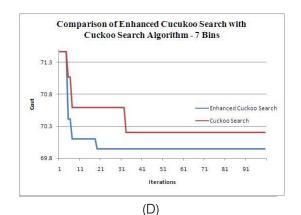


Fig.6 : Values obtained for 250 Strings

,399.71 ,402.41 ,399.47 1410 CS-10 Nests 187.97 .55 1400 CS-40 Nests ECS - 10 Nests with 4 Eggs 1390 70.65 555 1380 866.02 1370 1360 1350 1340 1330 4 5 6 8 9 10 Number of Bins

Fig. 7: Total Membership invalidation cost for CS and ECS

VIII. CONCLUSION

BFs are simple randomized data structures that are useful in practice. The BBF is an extension of BF, and inherits the best feature of BF such as time and space saving. The BBF treats strings in a set in a different way depending on their significance, groups the strings into bins and allocates different false positive rate to different bins. Important spam words have lower false positive rate than less significant words. In this work, we have applied CS and ECS for optimization of BF. The proposed system ECS outperforms CS.

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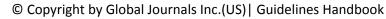
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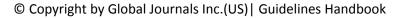
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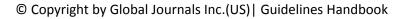
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